

Higher-level implicit parallelism with PASL

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What is PASL?

- PASL is our Parallel Algorithm Scheduling Library.
- It's a test bed for new ideas relating to implicit parallelism.
- It's written in C++.

How do we raise the level of abstraction?

Generalize the implicit-threading model

Primitives for creating and scheduling parallel computations

Address performance

Granularity control by Oracle Scheduling

Dynamic load balancing by work stealing with private deques

Primitives for creating and scheduling parallel computations

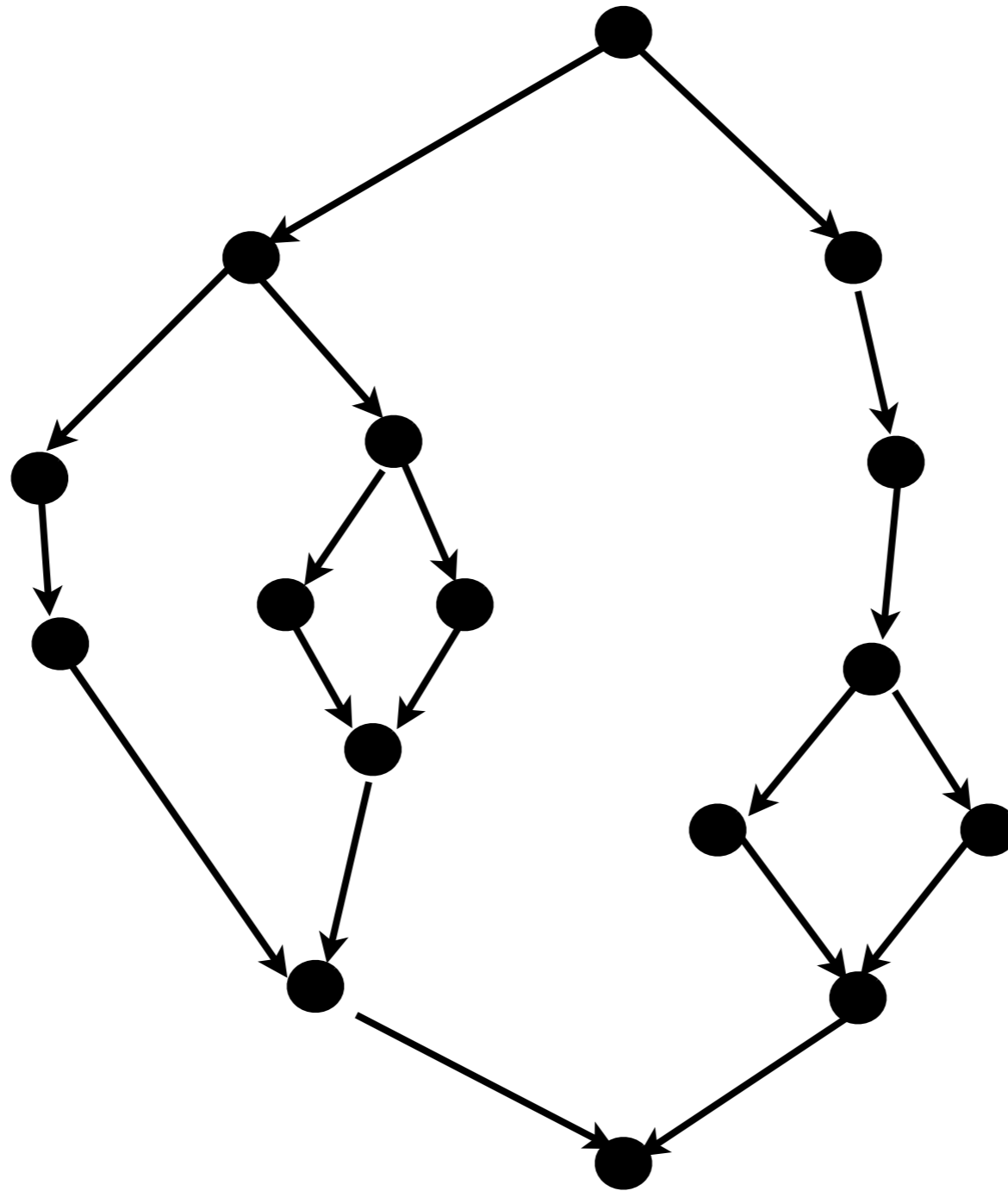
The implicit-parallelism zoo

- spawn/sync
- futures
- parallel loops
- TBB flow graphs
- reducers / hyperobjects
- map-reduce
- clocks / phasers
- concurrent revisions
- etc.

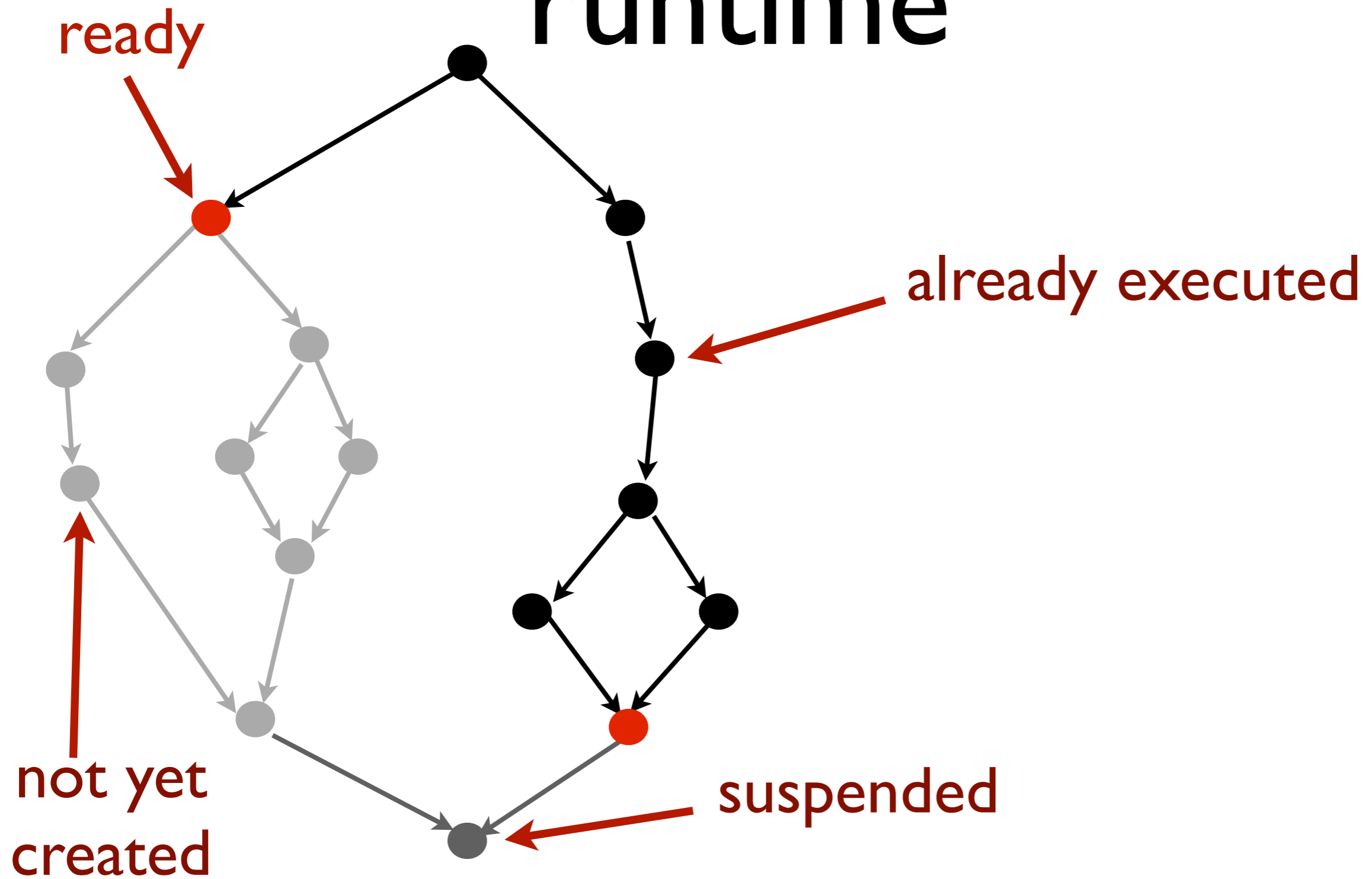
Computation DAGs

work

span



Computation DAGs at runtime



Almost-complete programming interface

```
node* create_node(closure*)
```

```
void add_node(node*)
```

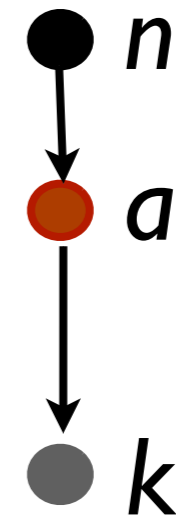
```
void add_edge(node*, node*)
```


Edge capture

n calling *a*
(with continuation *k*)

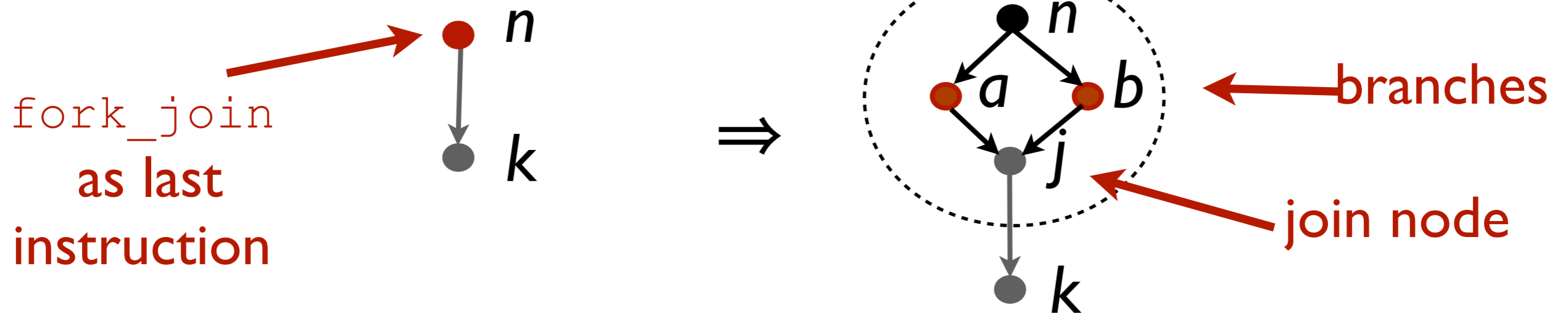


`transfer_outedges_to(a)`



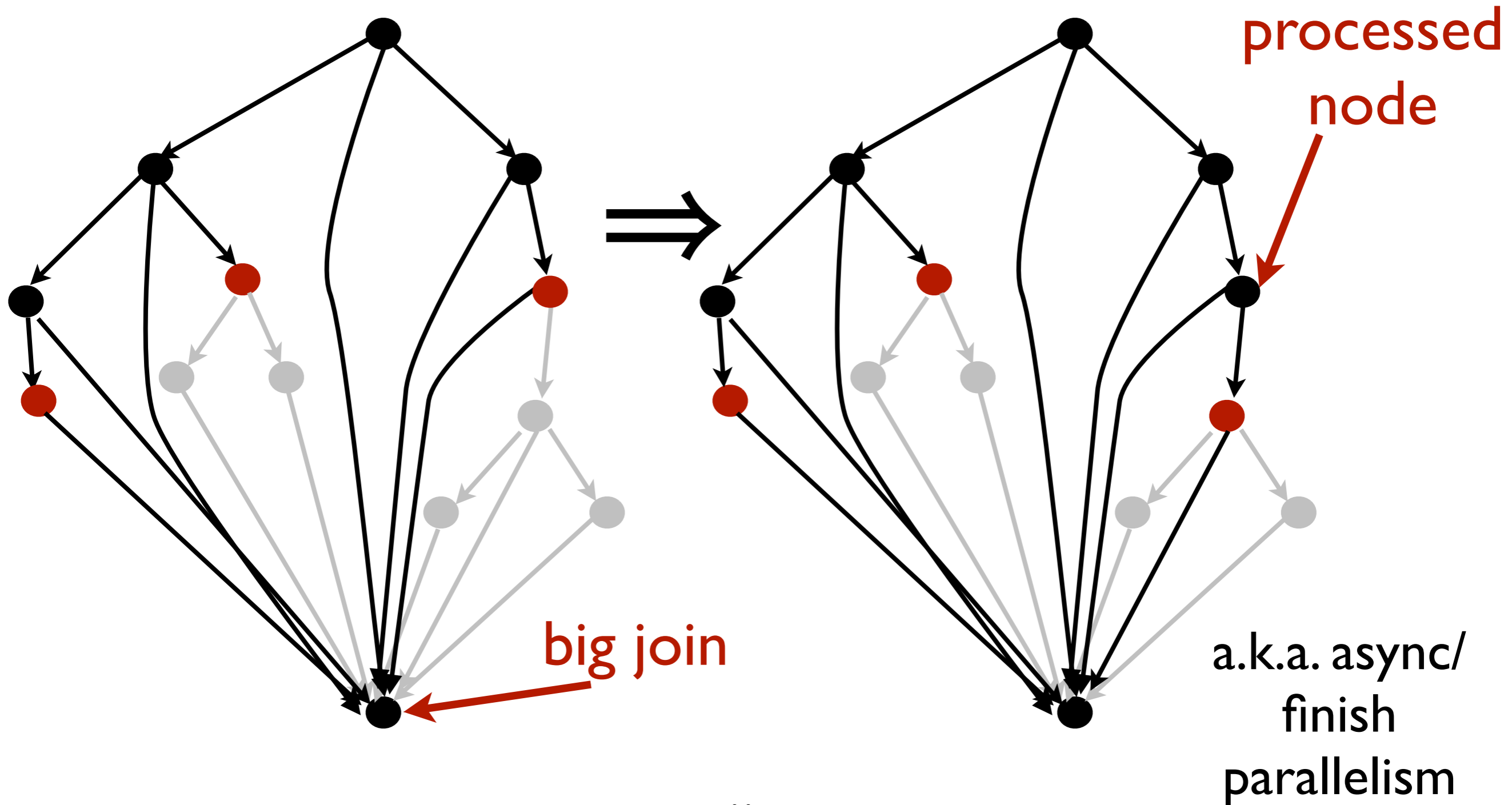
```
void transfer_outedges_to(node*)
```

Encoding binary fork join



```
void fork_join(closure* a, closure* b, closure* j)
  node* na = create_node(a)
  node* nb = create_node(b)
  node* nj = create_node(j)
  transfer_outedges_to(nj)
  add_edge(na, nj)
  add_edge(nb, nj)
  add_node(na)
  add_node(nb)
  add_node(nj)
```

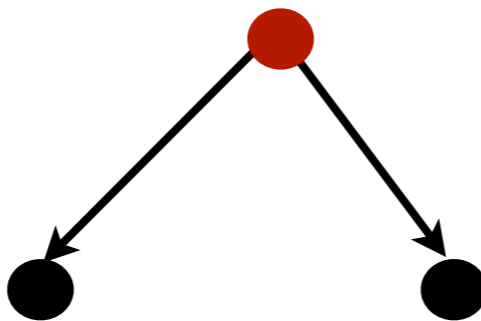
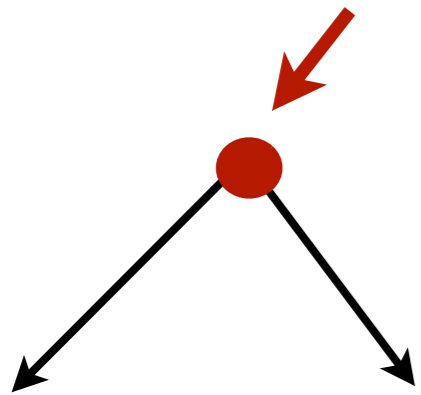
Encoding graph traversal using a big join



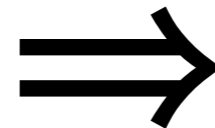
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Encoding futures

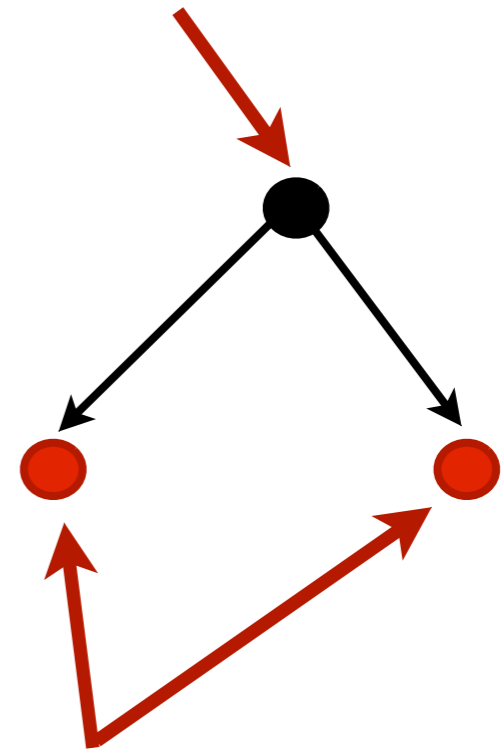
future (producer)



consumer
demanding the
future be forced



future executed



become ready

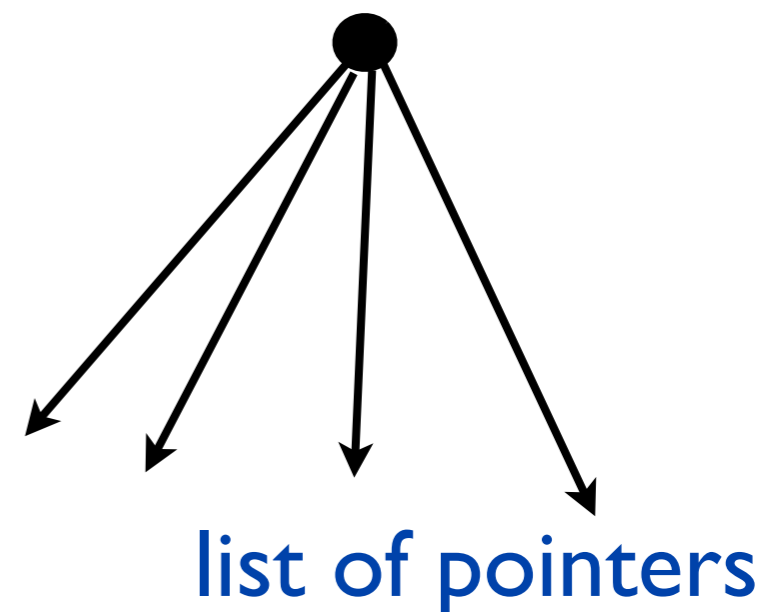
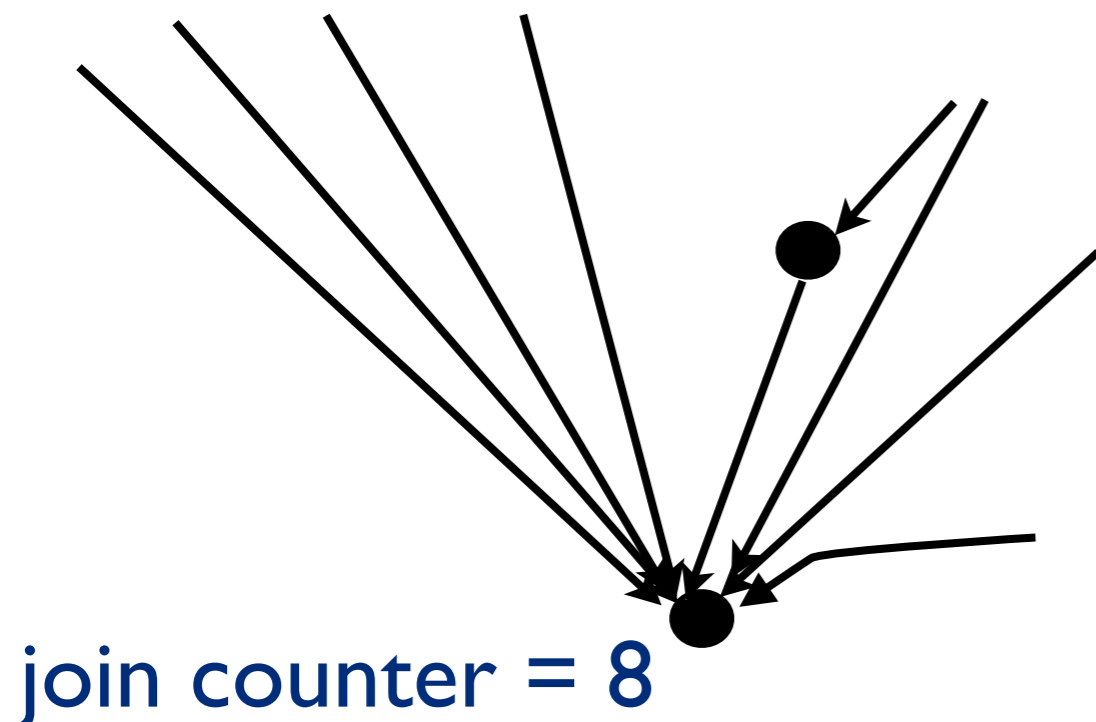
Four key ingredients for efficiency

1. Granularity control

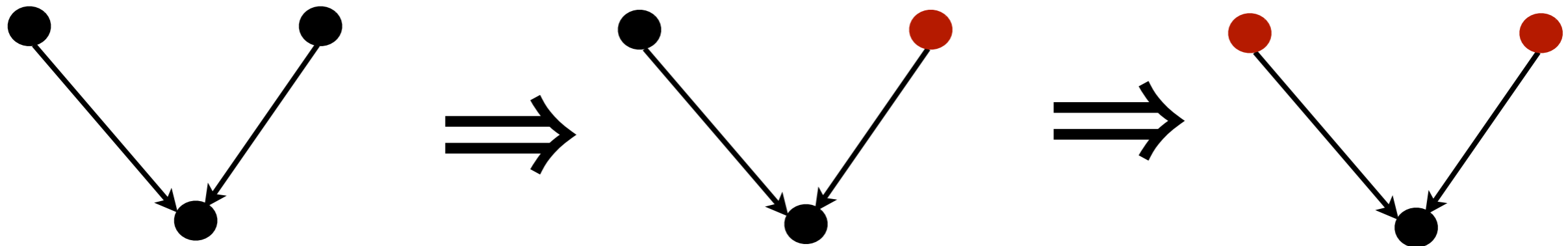
2. Dynamic load balancing (work stealing)

3. Number of incoming edges (a.k.a. join counter)

4. Continuation (list of edges)



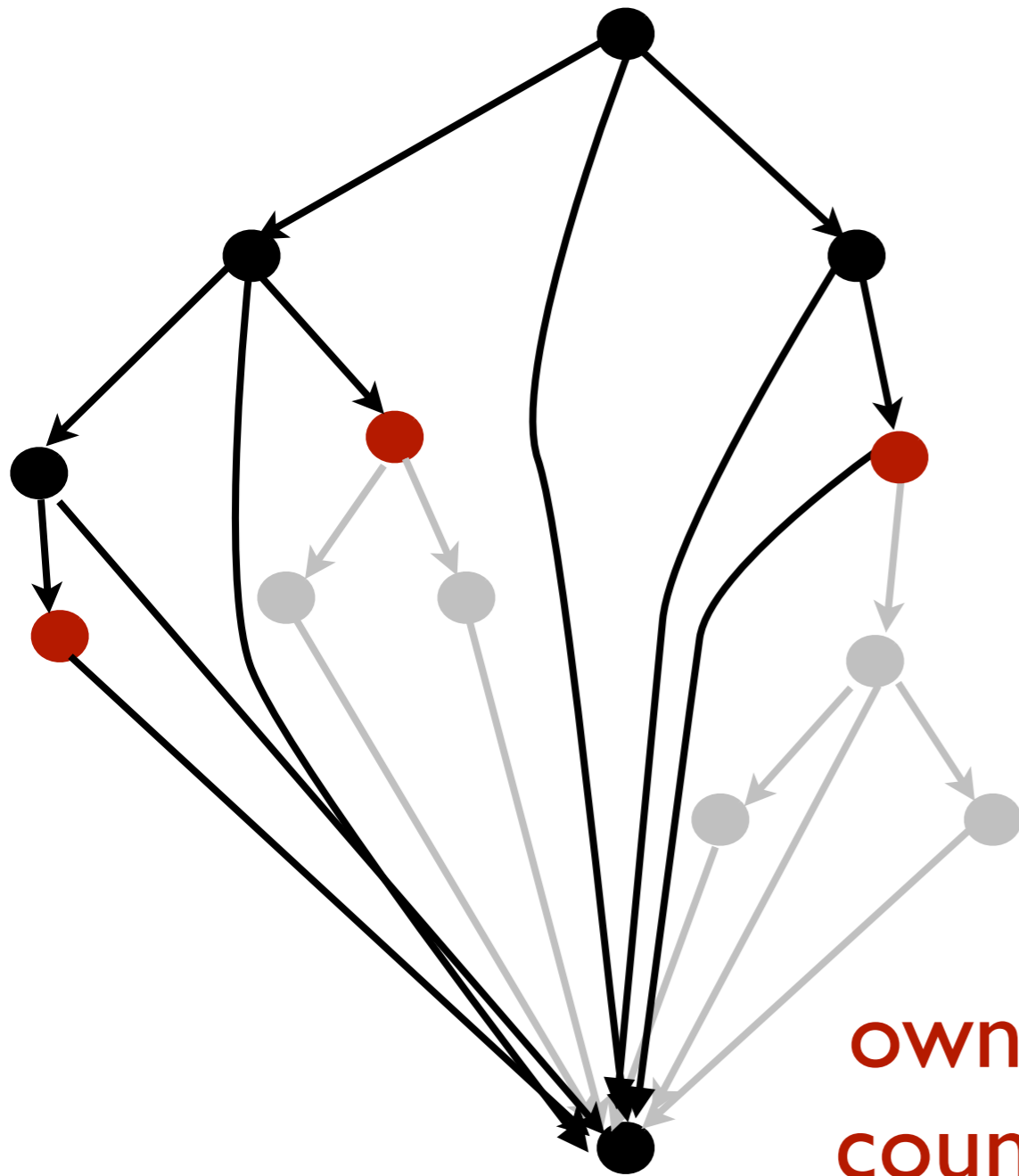
Small-arity joins with atomic counters



join counter = 2 join counter = 1 join counter = 0


`fetch_and_add(-1)`

Big-arity joins



- use one counter per processor:
- # edges added - # edges removed
- periodic check by one particular processor to see if the sum is zero

owner = core #4
counters = [23; -9; 97; 67; 20]

Representation of nodes and edges

- We use an *instrategy* for representing the number of incoming edges
- and an *outstrategy* for representing the list of outgoing edges

```
node* create_node(closure*, instrategy*, outstrategy*)
```


Summary

Dynamic DAGs, with per-node specification of edge representation:

```
node* create_node(closure*, instrategy*, outstrategy*)
void add_node(node*)
void add_edge(node*, node*)
void transfer_edges_to(node*)
```

Find other examples of custom instrategies in paper, e.g.,

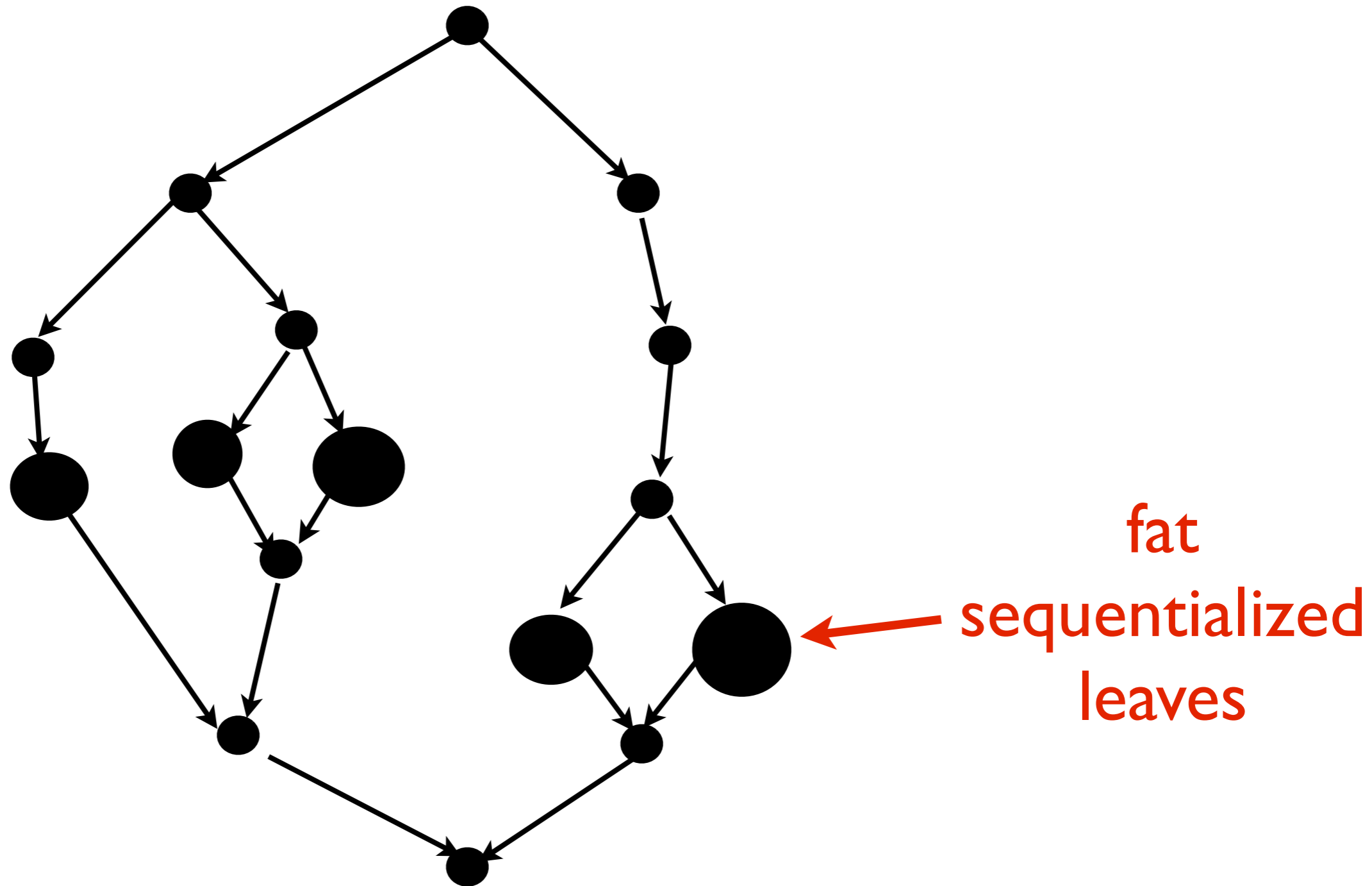
- distributed
- owner based
- optimistic

Automatic granularity control by Oracle Scheduling

Do we need to tame DAG-related overheads?

- Yes:
 - Parallel fib in PASL is typically 100x slower than sequential fib.
 - Parallel fib in PASL is no more than a few percent slower.
- It's not that bad, because we can ensure the costs are well amortized by granularity control.

Taming DAG overheads



Oracle scheduling

Idea: 1. Pick a target leaf run time t .

```
void quicksort(int A[], int s, int e) {  
    if (e - s < 2)  
        return;  
    int p = partition(A, s, e);  
    fork_join {  
        quicksort (A, s, p);  
        quicksort (A, p + 1, e);  
    }  
}
```

2. Make calls:

- parallel, if combined run time prediction $> t$
- sequential, otherwise

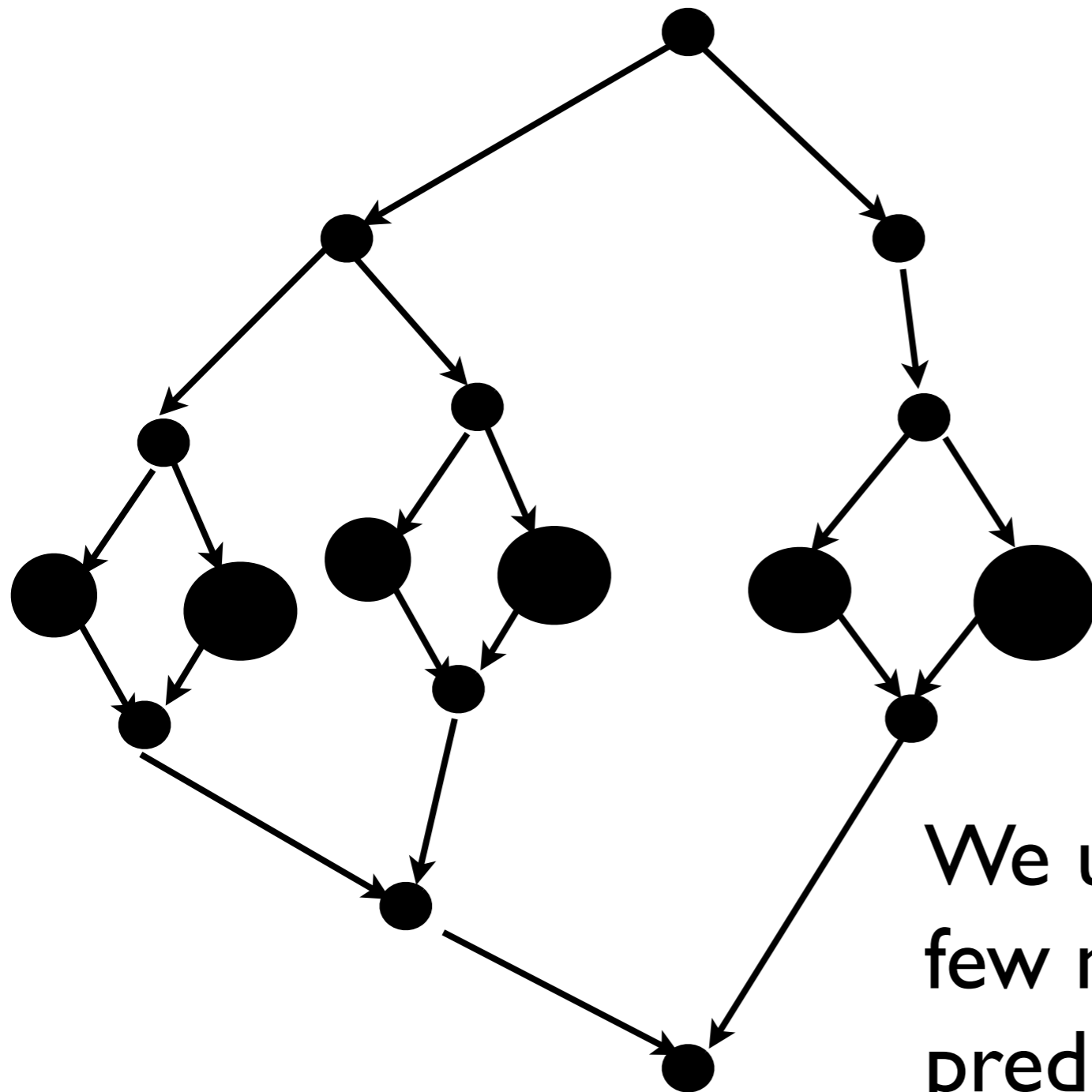
Our theoretical contribution

- Suppose we have an oracle predicting run times with error always less than certain ratio.
- Then, the total cost of creating nodes is well amortized.
- See paper for precise formal bound.

Complexity annotations

```
void quicksort(int A[], int s , int e) {  
    cost {  
        int n = e - s;  
        return n * log(n) ← complexity annotation  
    }  
    if (e - s < 2)  
        return;  
    int p = partition(A, s , e);  
    fork_join {  
        quicksort(A, s , p );  
        quicksort(A, p + 1, e );  
    }  
}
```

Runtime profiling



Let n be asymptotic complexity of a call.

Let $r = e / n$.

measure
execution
time e

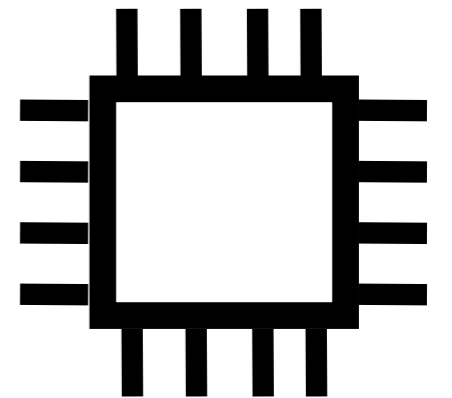
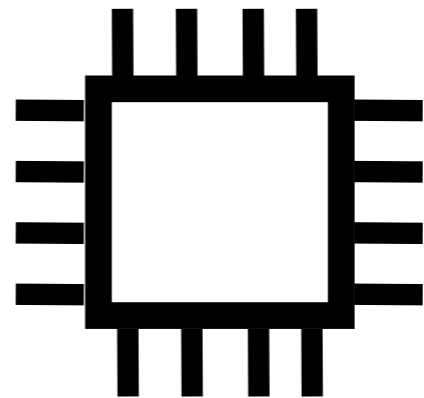
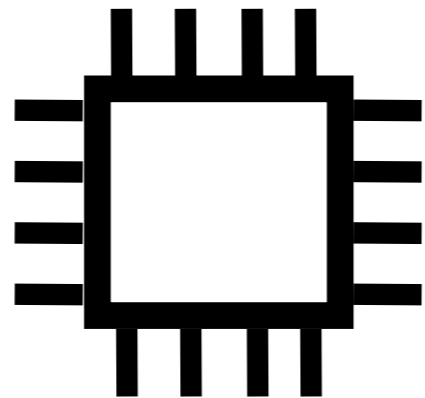
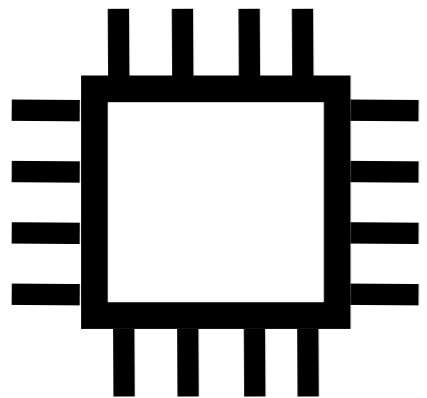
We use running average of past few measurements of r to make predictions.

Summary

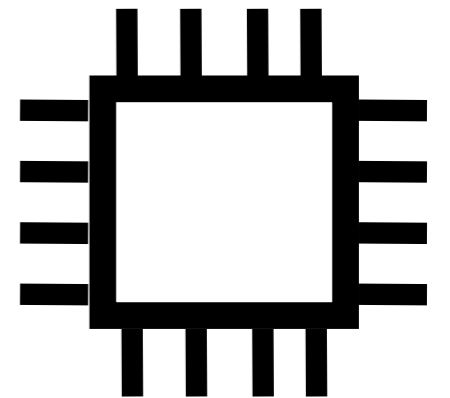
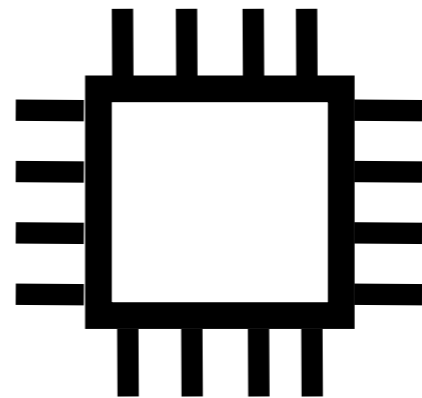
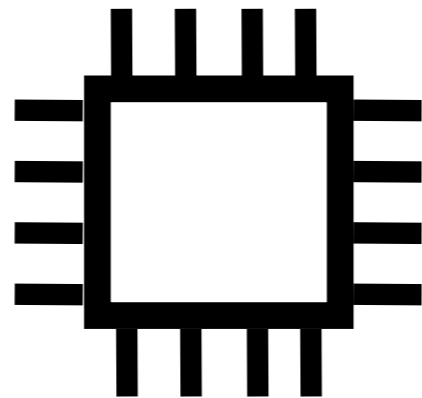
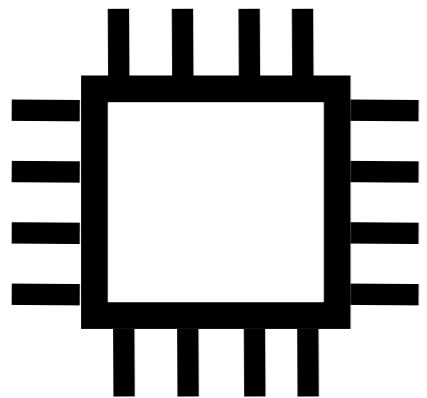
- A few issues:
 - Outlier measurements increase error.
 - Our approach assumes that average case complexity matches worst case.
- Our approach works well for a wide range of computations.
- Please see our paper for performance study.

Dynamic load balancing by work stealing with private dequeues

Scheduling parallel tasks



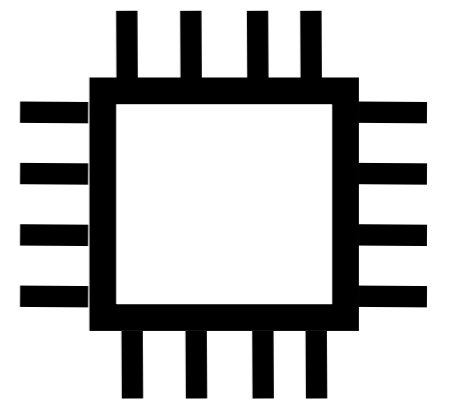
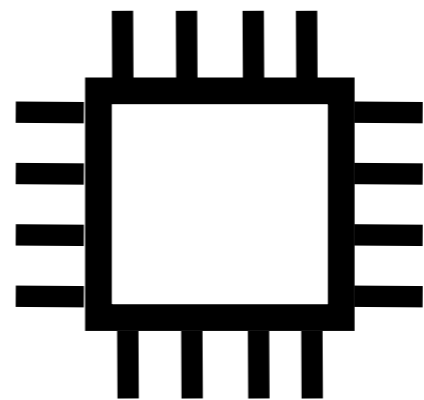
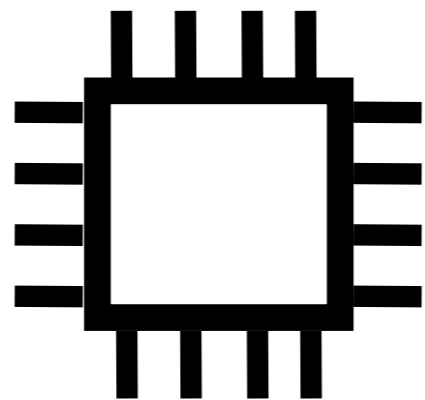
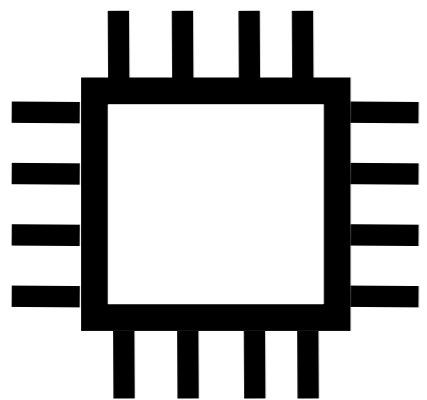
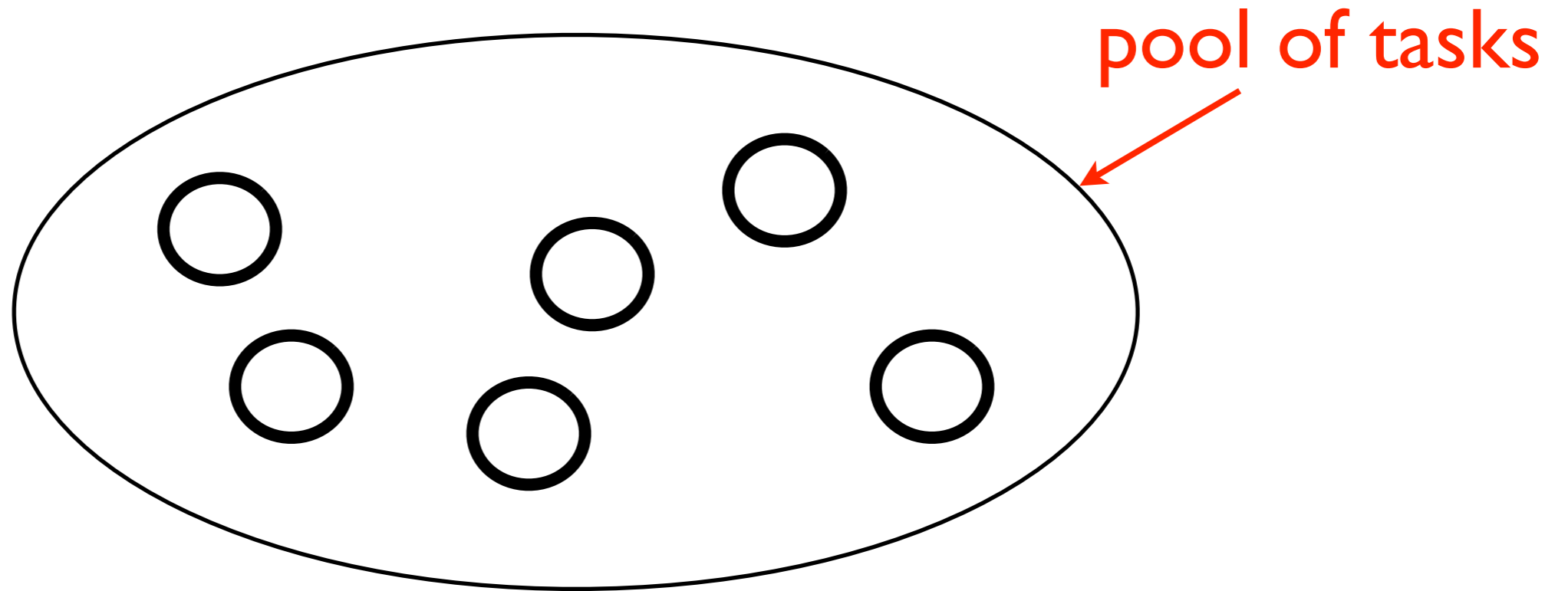
Scheduling parallel tasks



set of cores



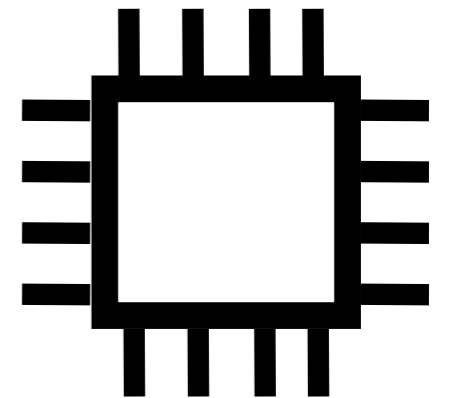
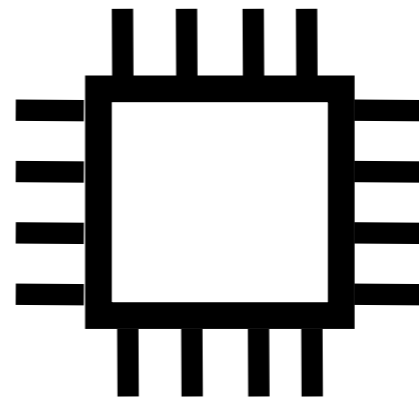
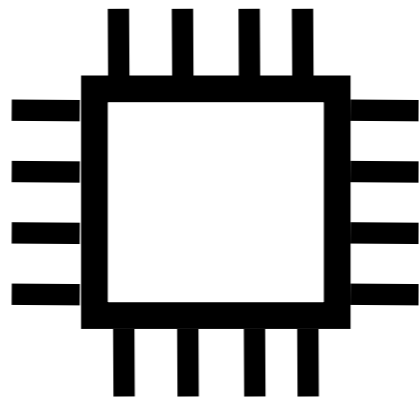
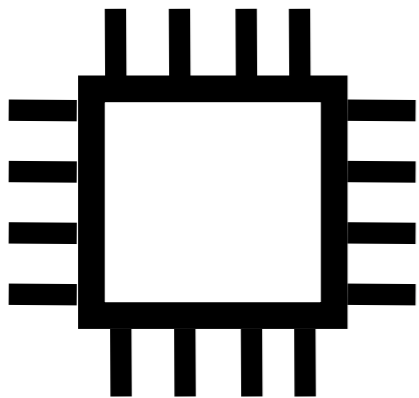
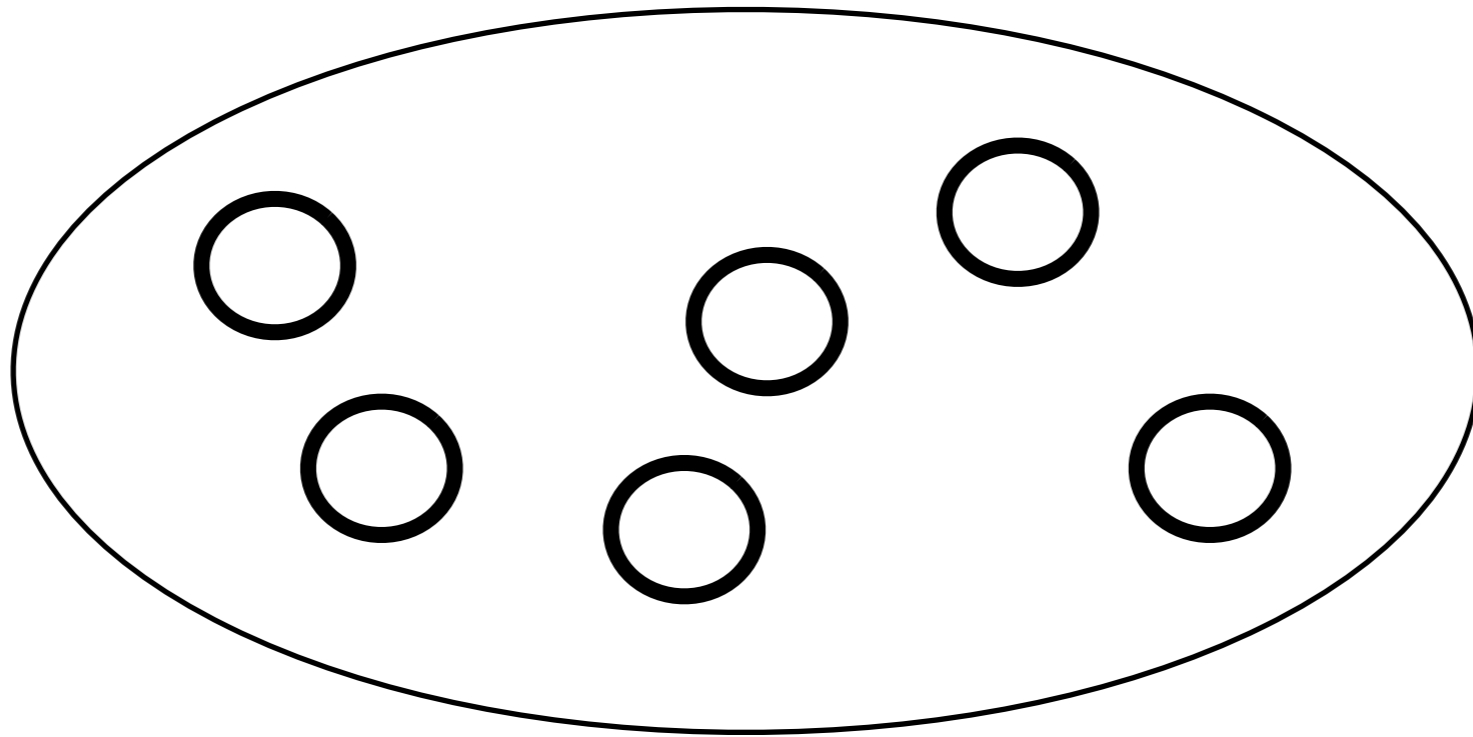
Scheduling parallel tasks



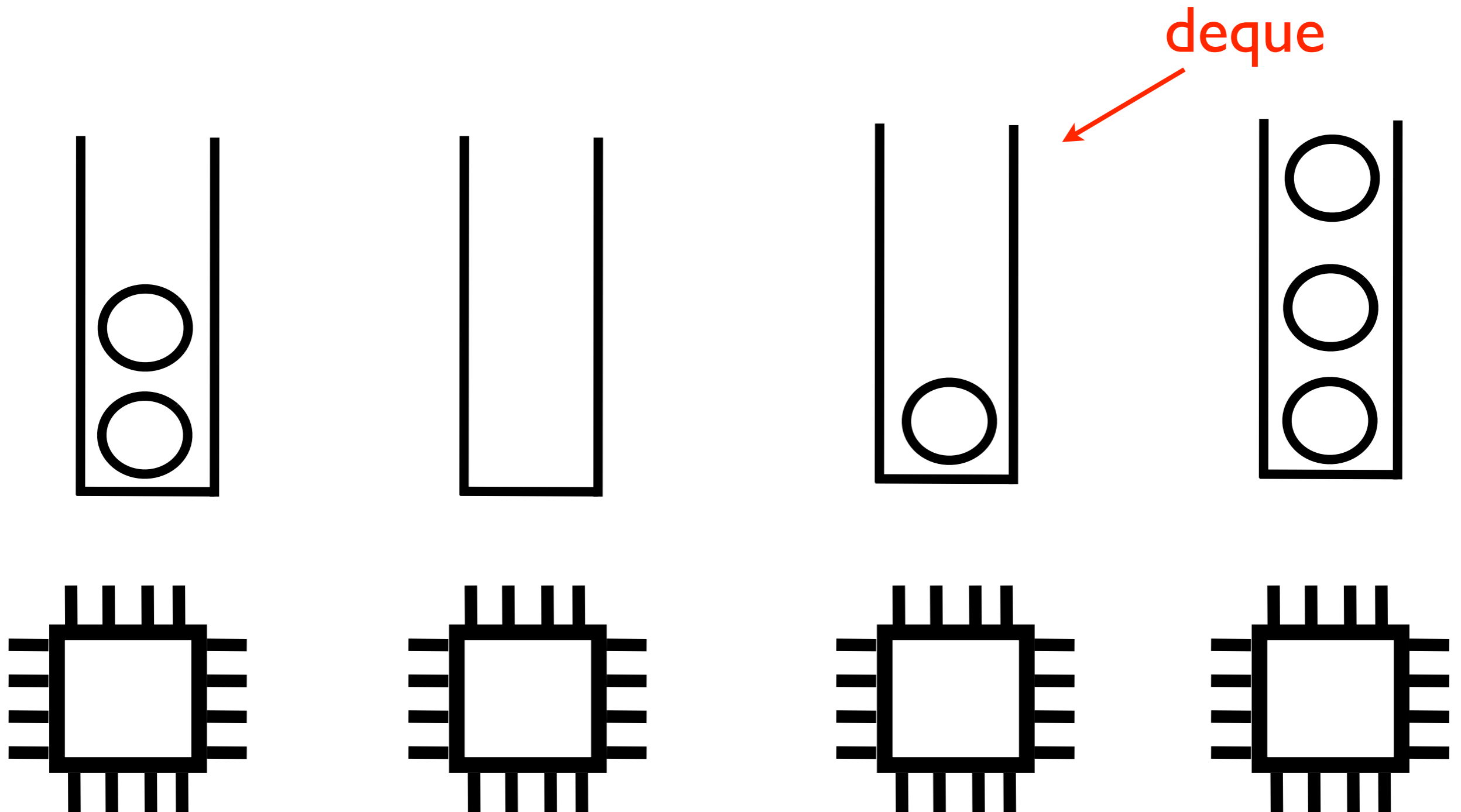
Scheduling parallel tasks

- Goal: dynamic load balancing
- A centralized approach: does not scale up
- Popular approach: work stealing
- Our work: study implementations of work stealing

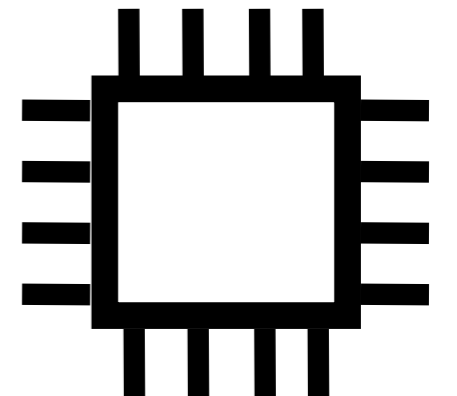
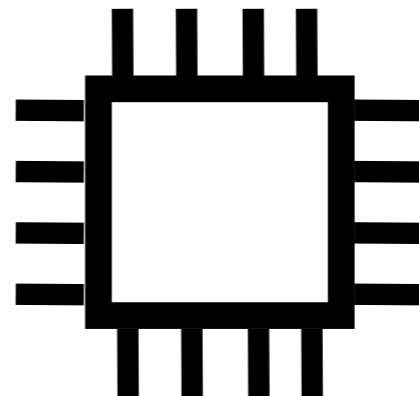
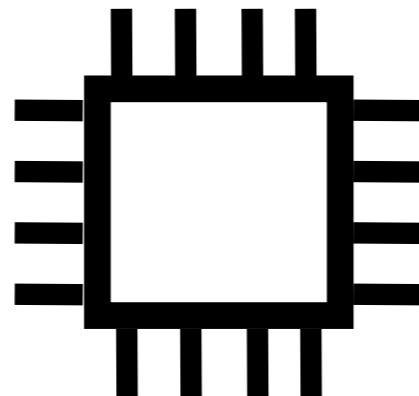
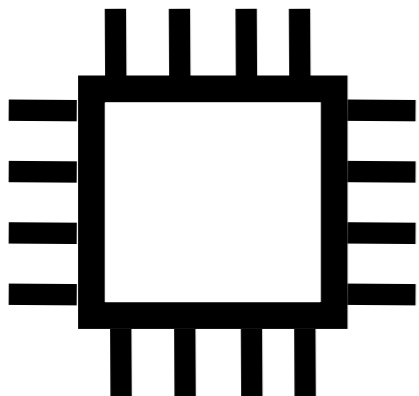
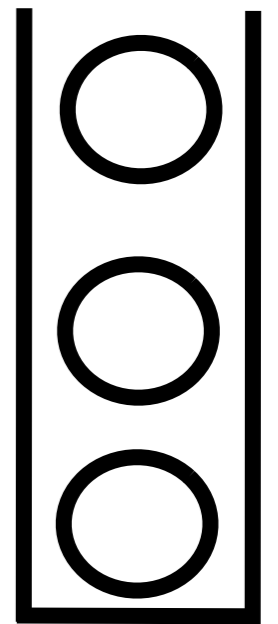
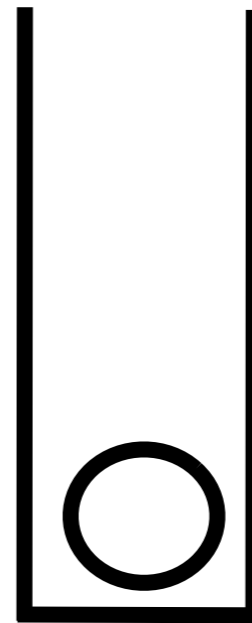
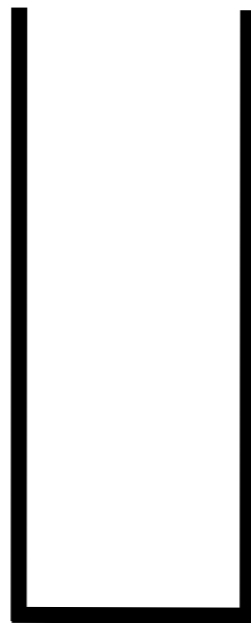
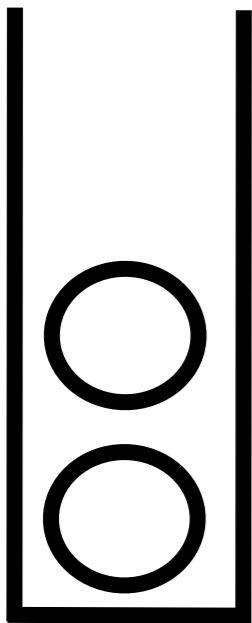
Work stealing



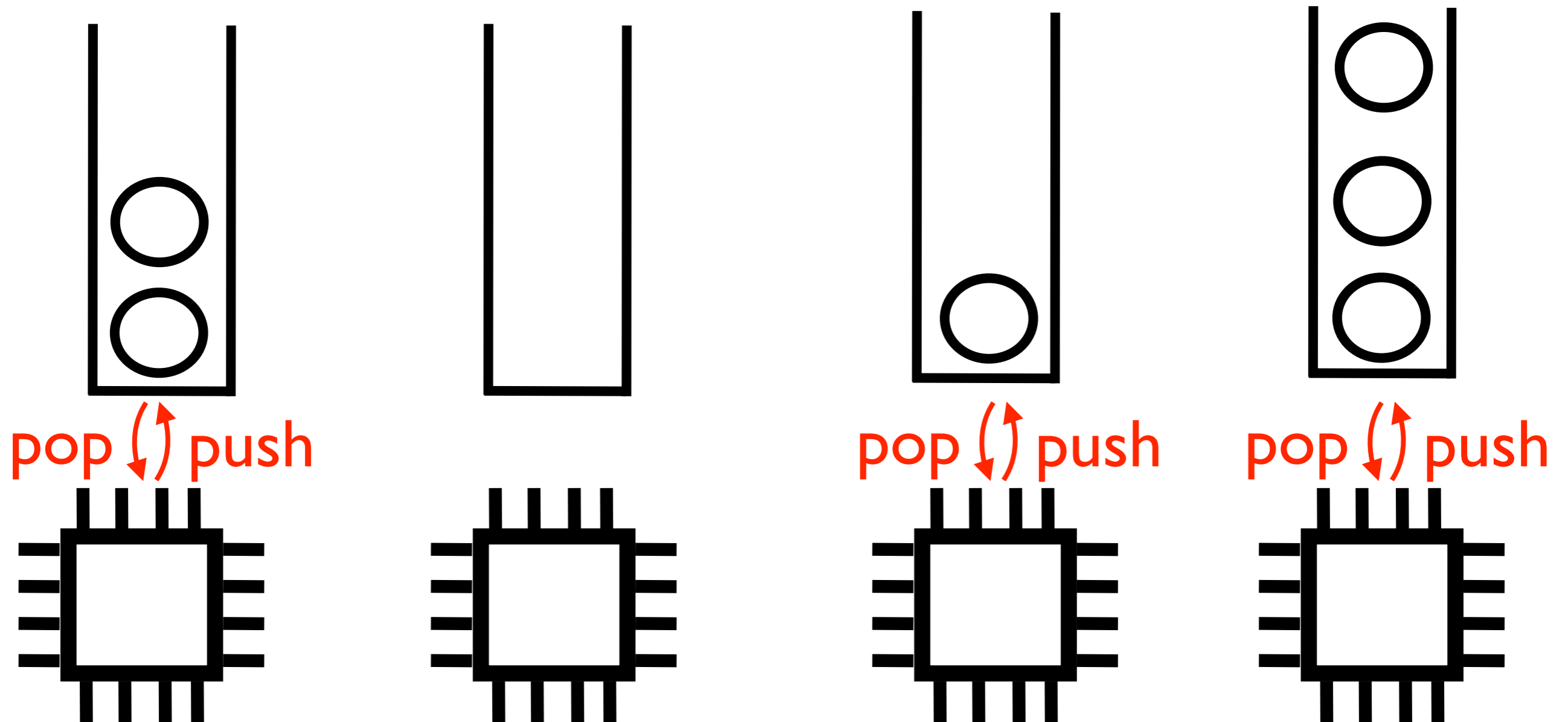
Work stealing



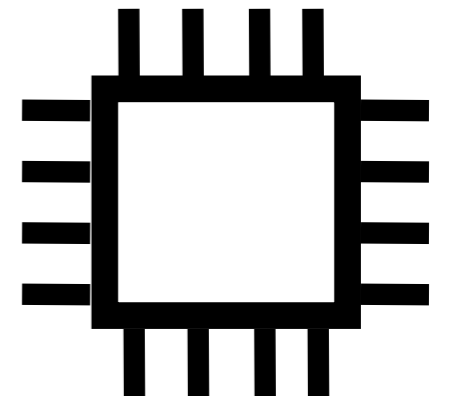
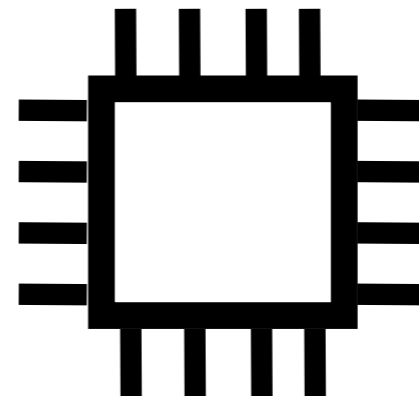
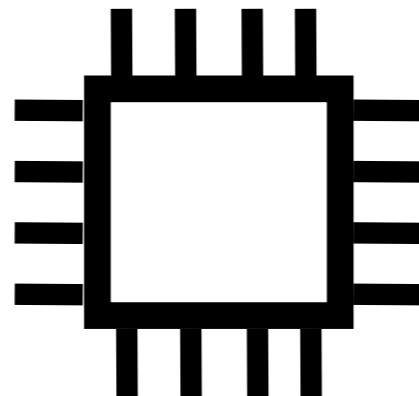
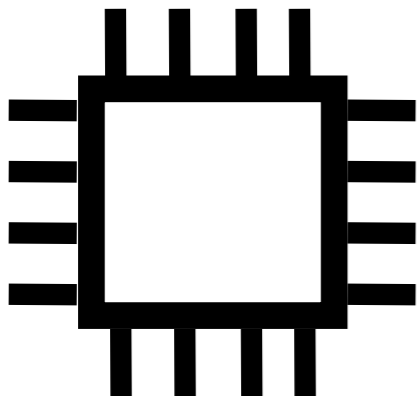
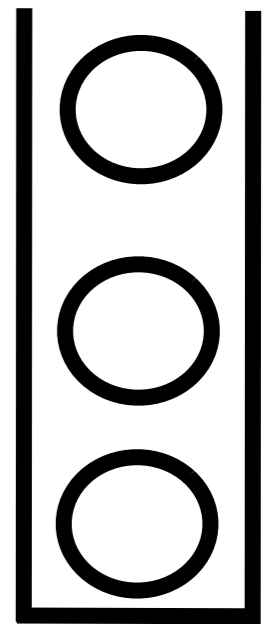
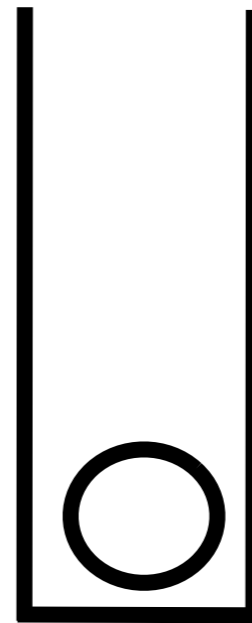
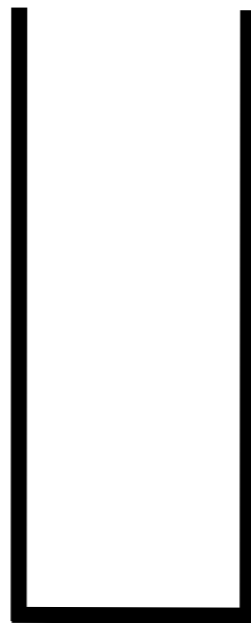
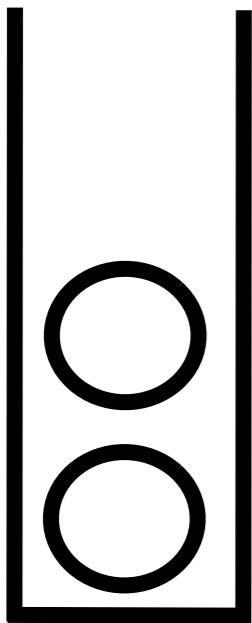
Work stealing



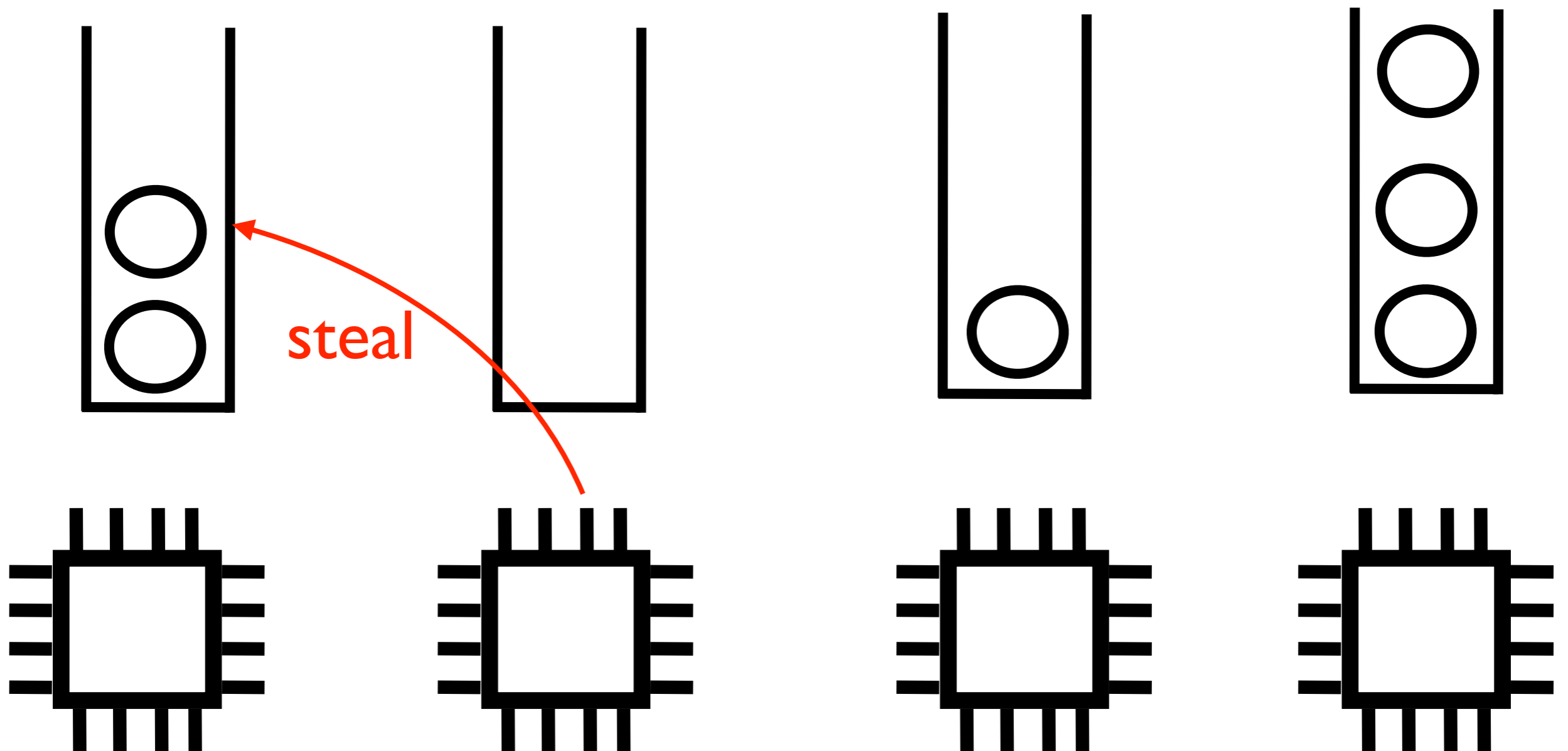
Work stealing



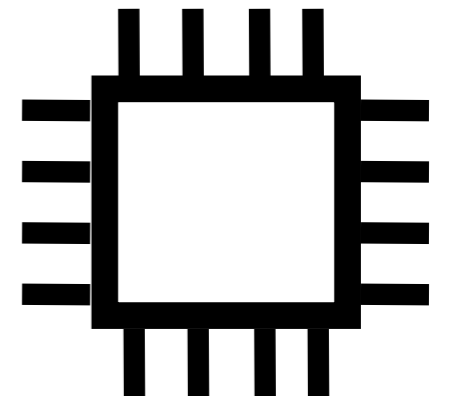
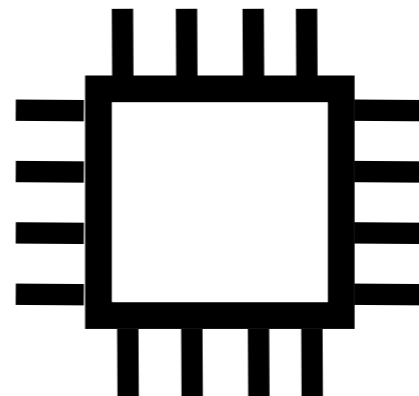
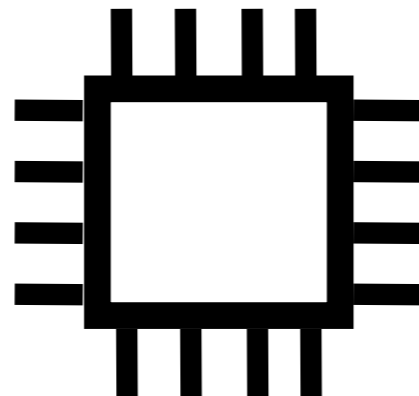
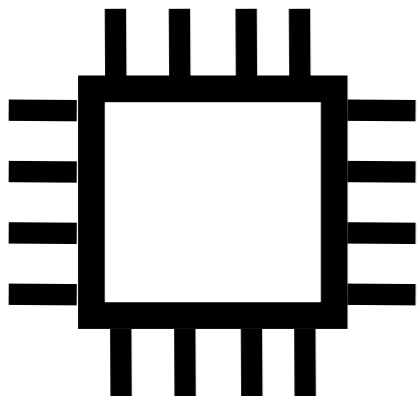
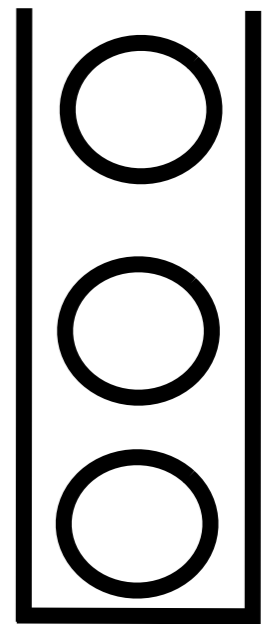
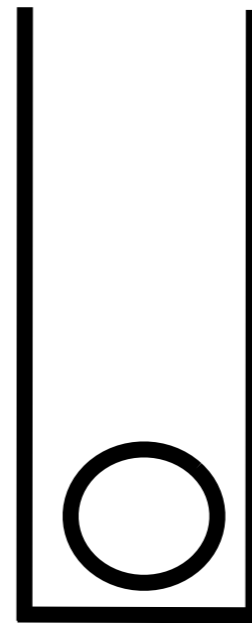
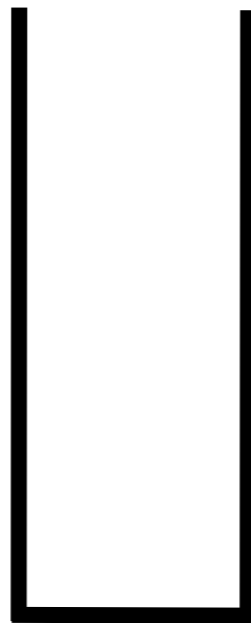
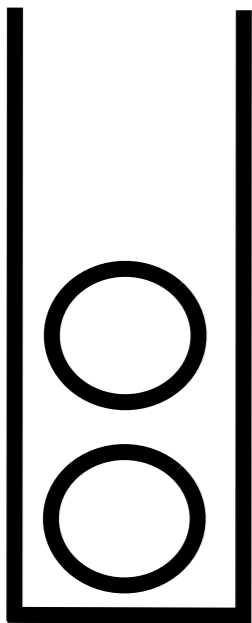
Work stealing



Work stealing

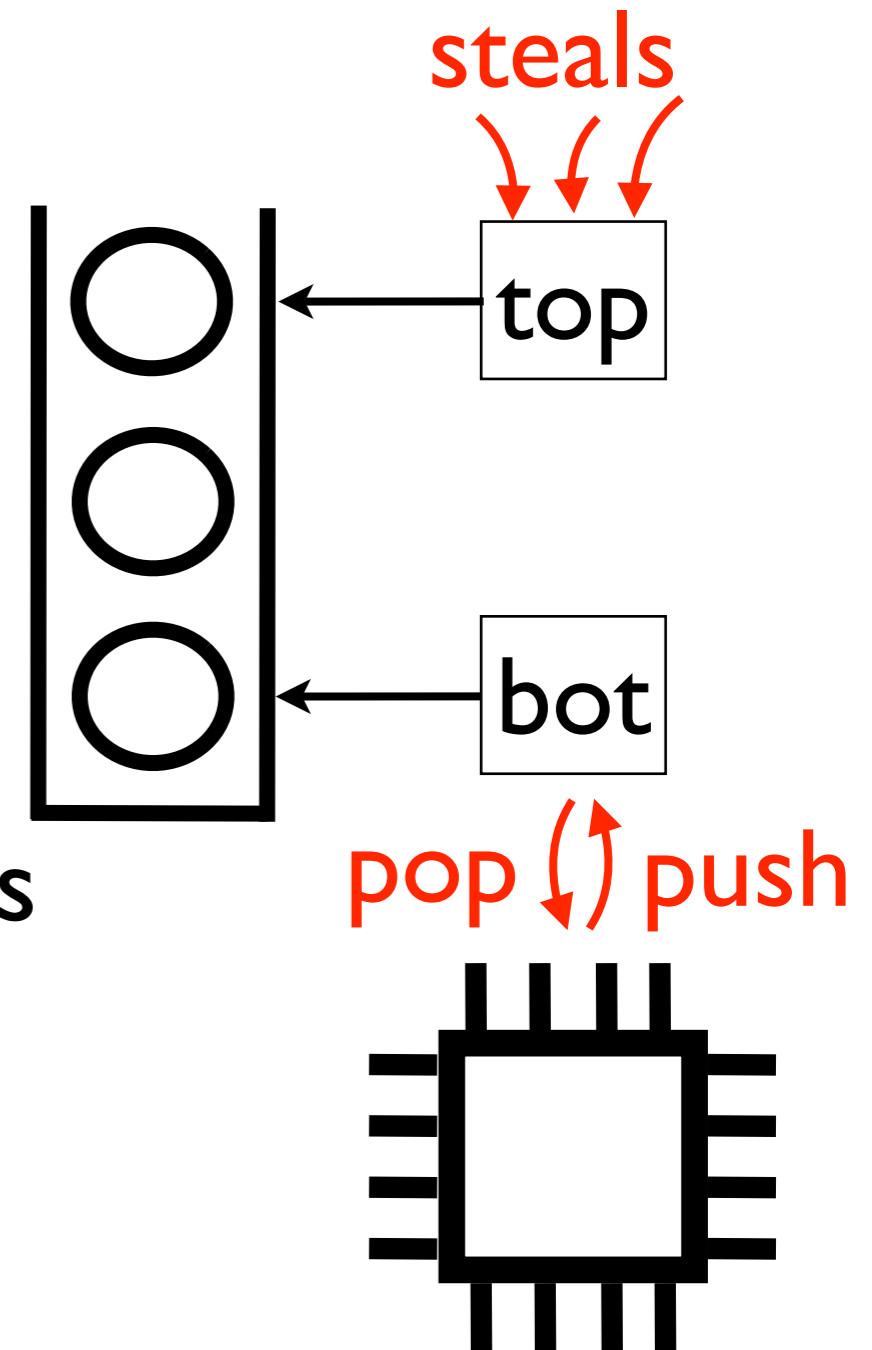


Work stealing



Concurrent dequeues

- Deques are shared.
- Two sources of race:
 - between thieves
 - between owner and thief
- Chase-Lev data structure resolves these races using atomic compare&swap and memory fences.



Concurrent dequeues

- **Well studied:** shown to perform well both in theory and in practice ...

however, researchers identified two main limitations

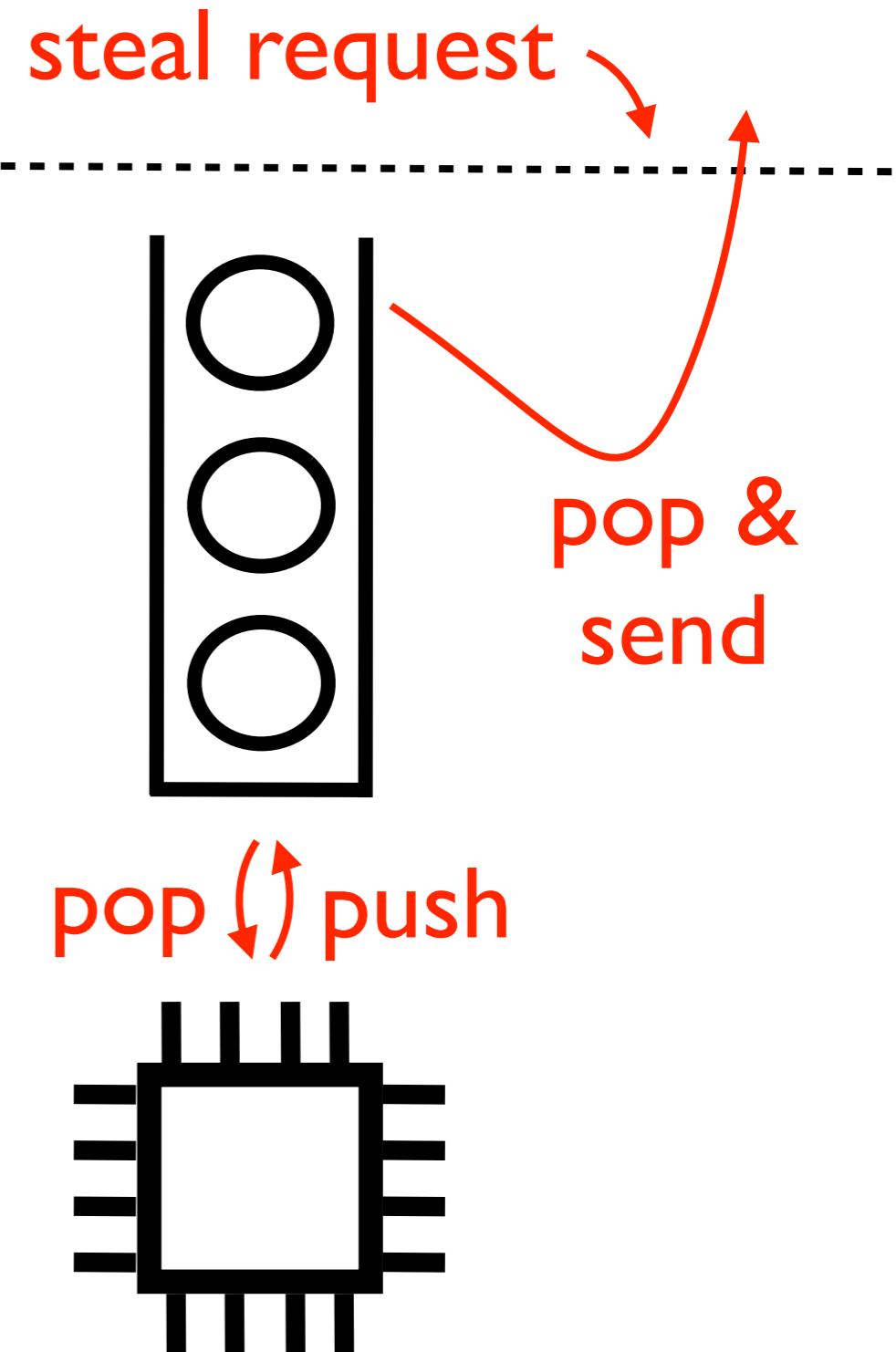
- **Runtime overhead:** In a relaxed memory model, `pop` must use a memory fence.
- **Lack of flexibility:** Simple extensions (e.g., steal half) involve major challenges.

Previous studies of private dequeues

Feeley	1992	Multilisp
Hendler & Shavit	2002	C
Umatani	2003	Java
Hirashi et al.	2009	C
Sanchez et al.	2010	C
Fluet et al.	2011	Parallel ML

Private dequeues

- Each core has exclusive access to its own deque.
- An idle core obtains a task by making a *steal request*.
- A busy core regularly checks for incoming requests.



Private dequeues

Addresses the main limitations of concurrent dequeues:

- no need for memory fence
- flexible dequeues (any data structure can be used)

but

- new cost associated with regular polling
- additional delay associated with steals

Unknowns of private dequeues

- What is the best way to implement work stealing with private dequeues?
- How does it compare on state of art benchmarks with concurrent dequeues?
- Can establish tight bounds on the runtime?

Unknowns of private dequeues

- What is the best way to implement work stealing with private dequeues?

We give a receiver- and a sender-initiated algorithm.

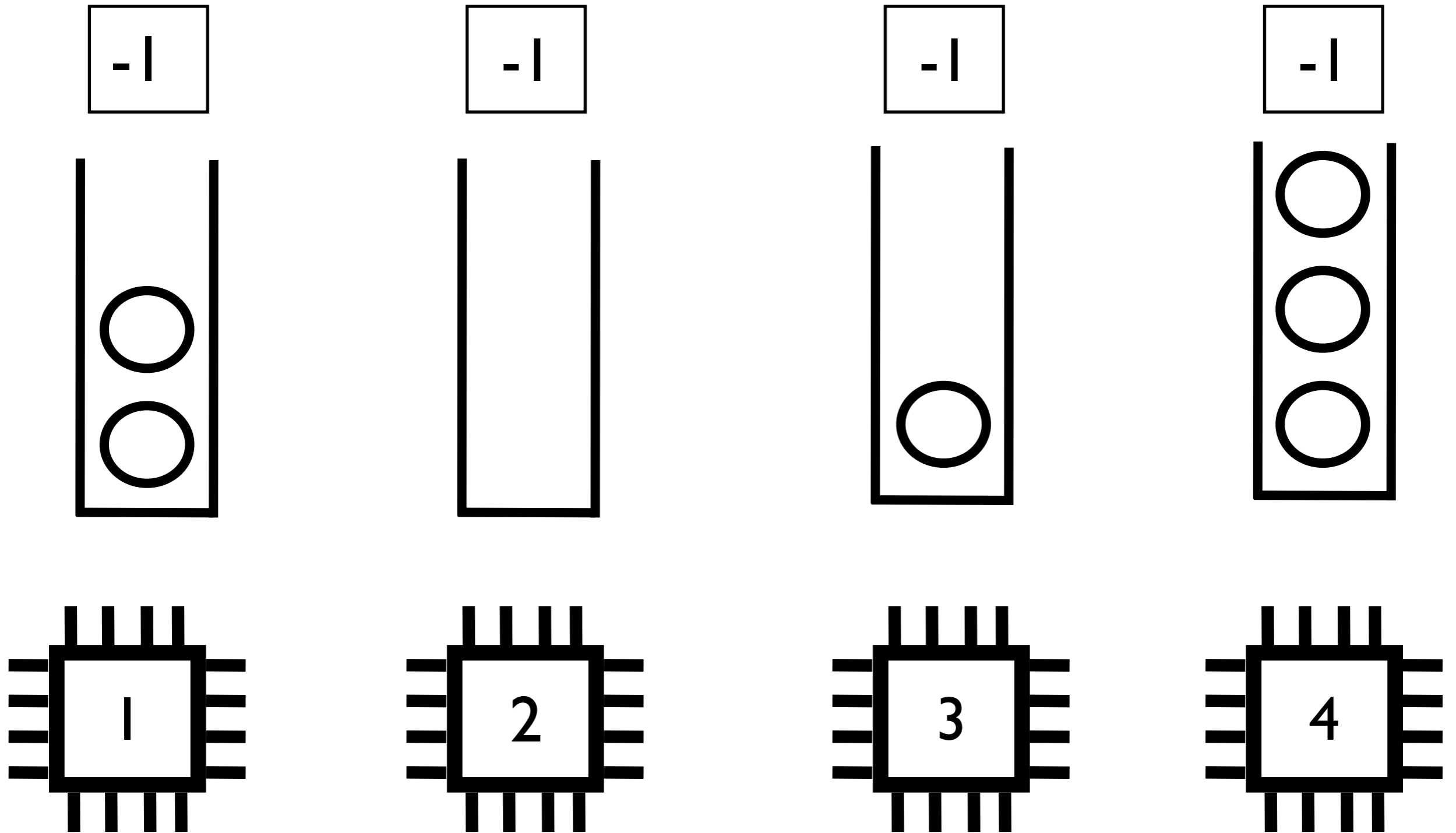
- How does it compare on state of art benchmarks with concurrent dequeues?

We evaluate on a collection of benchmarks.

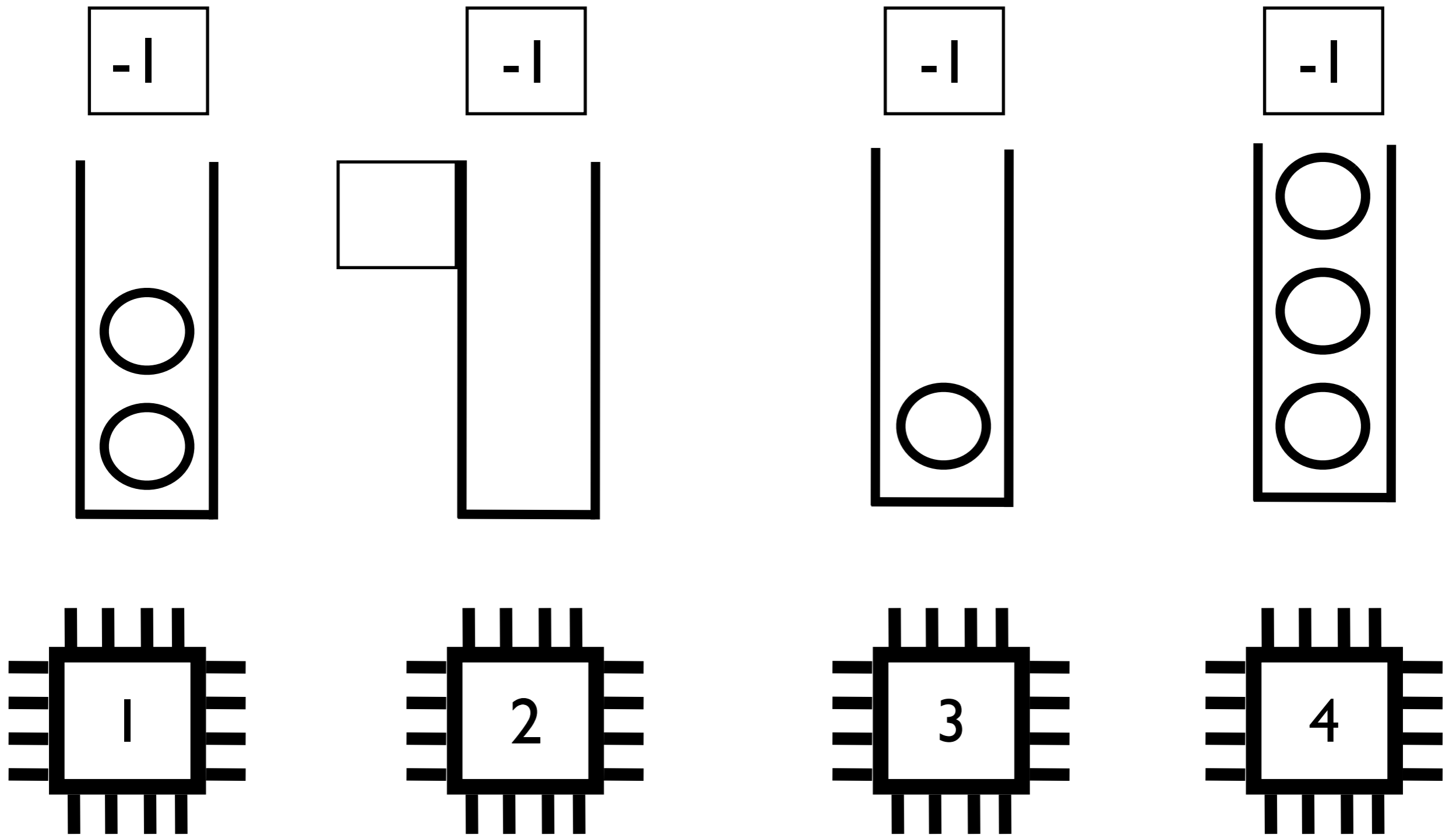
- Can establish tight bounds on the runtime?

We prove a theorem w.r.t. delay and polling overhead.

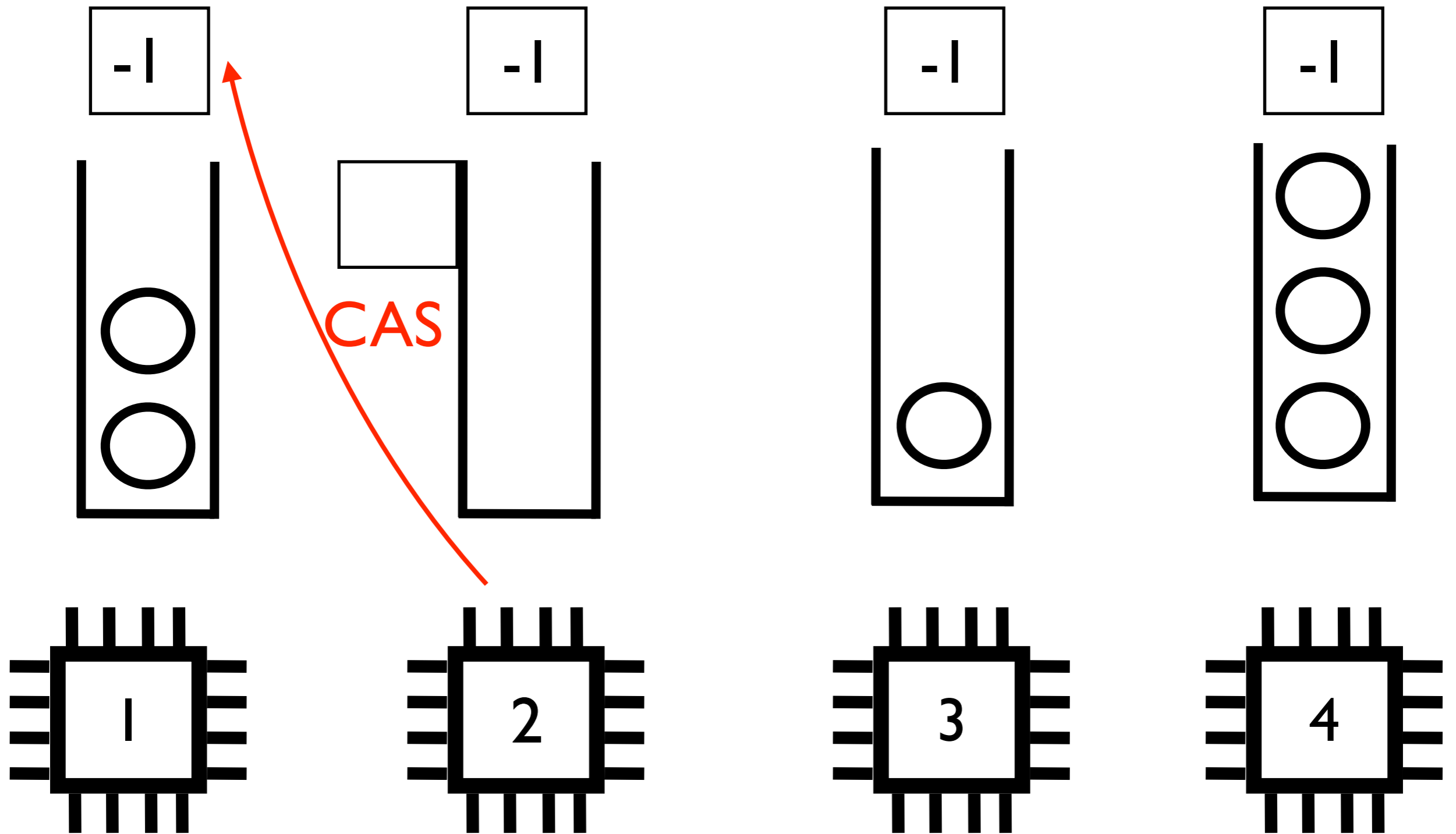
Receiver initiated



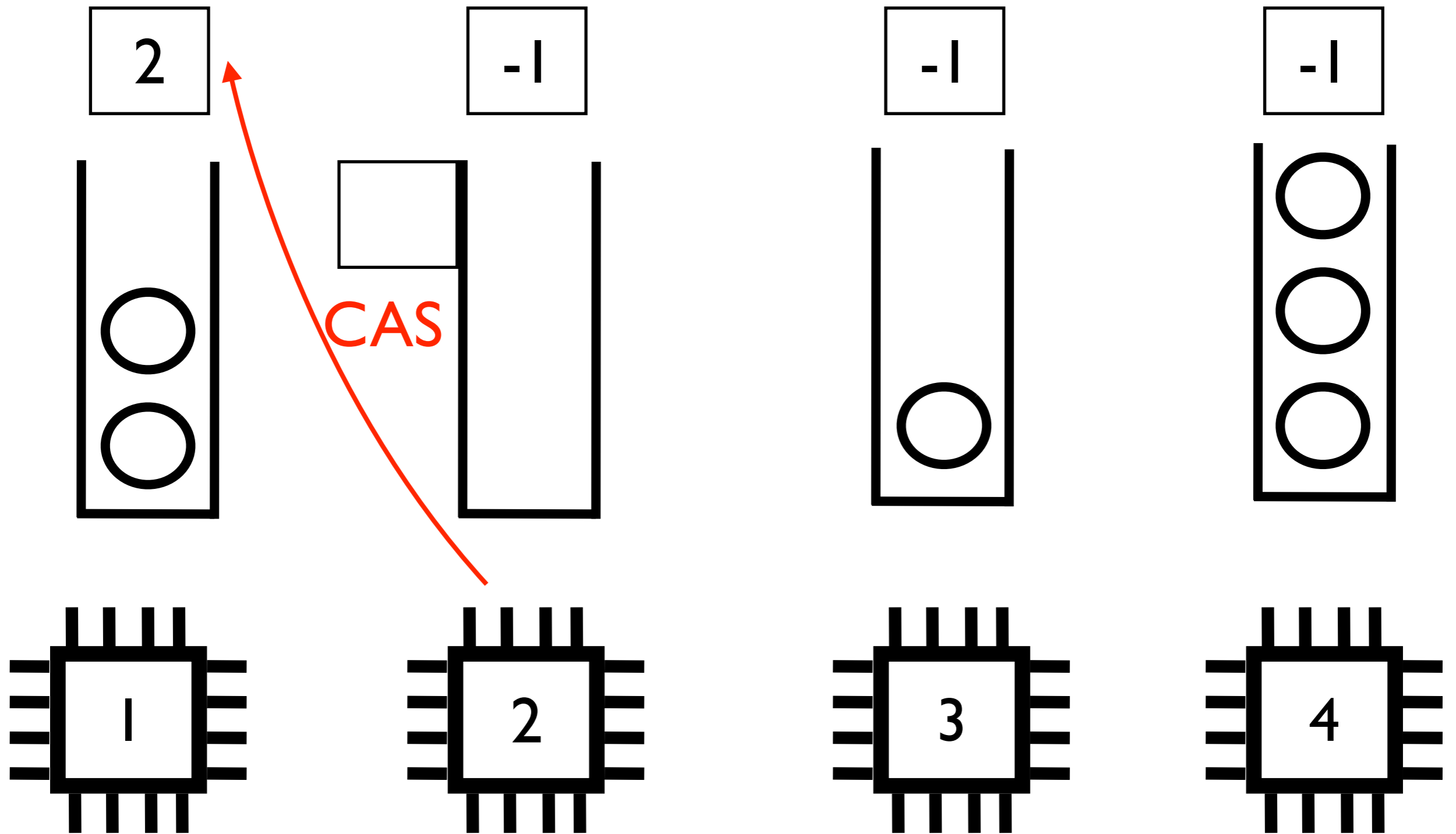
Receiver initiated



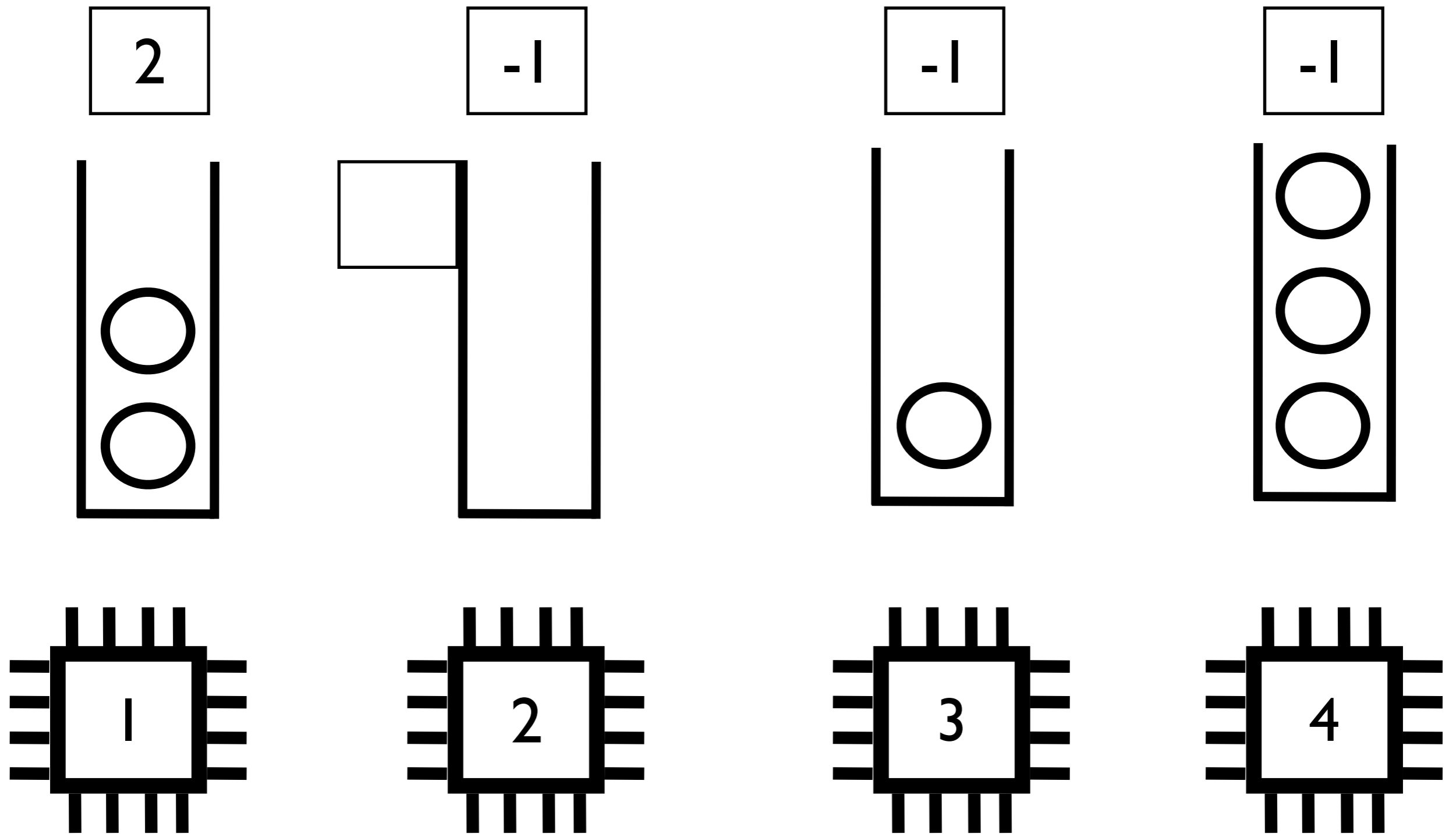
Receiver initiated



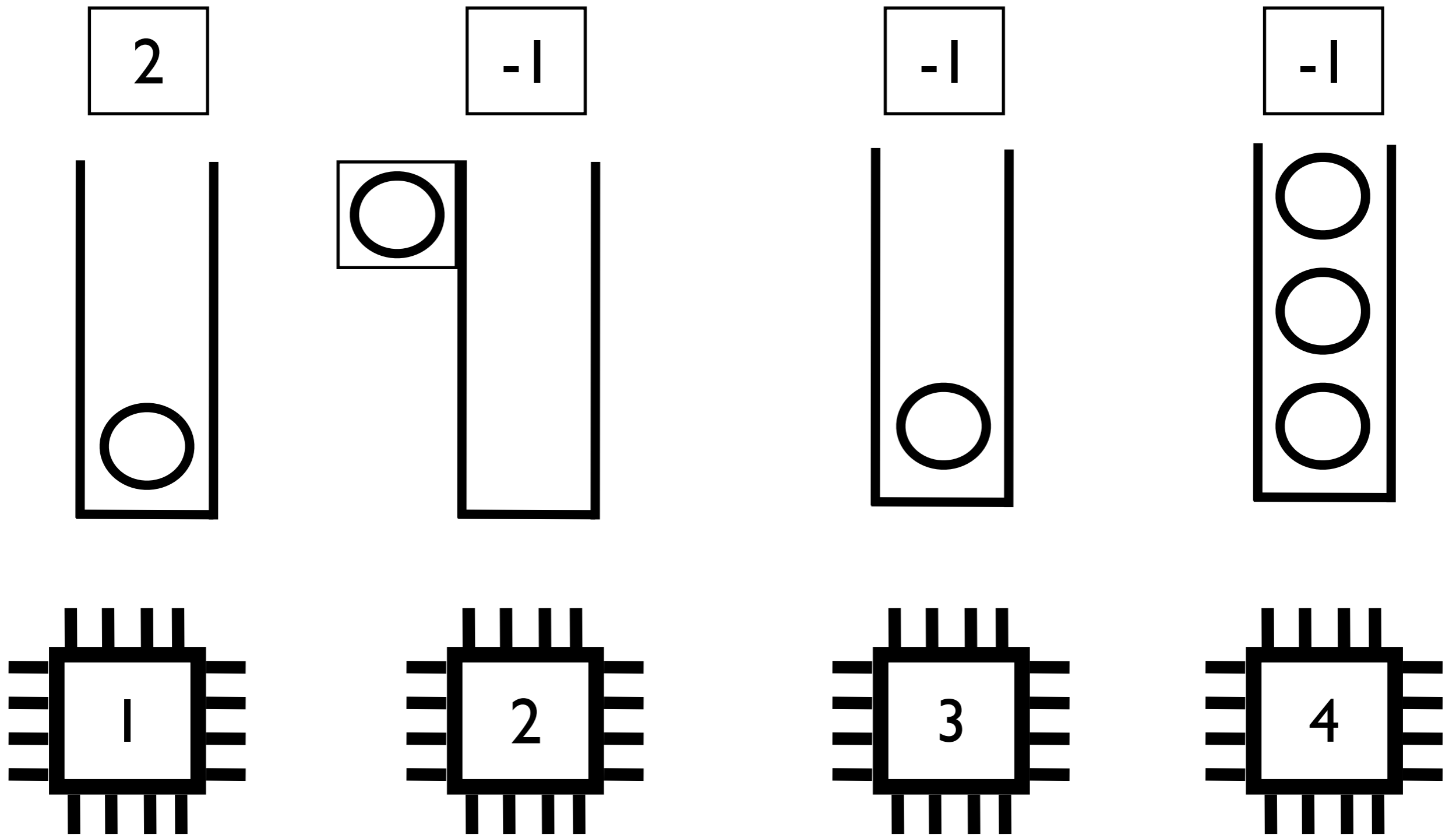
Receiver initiated



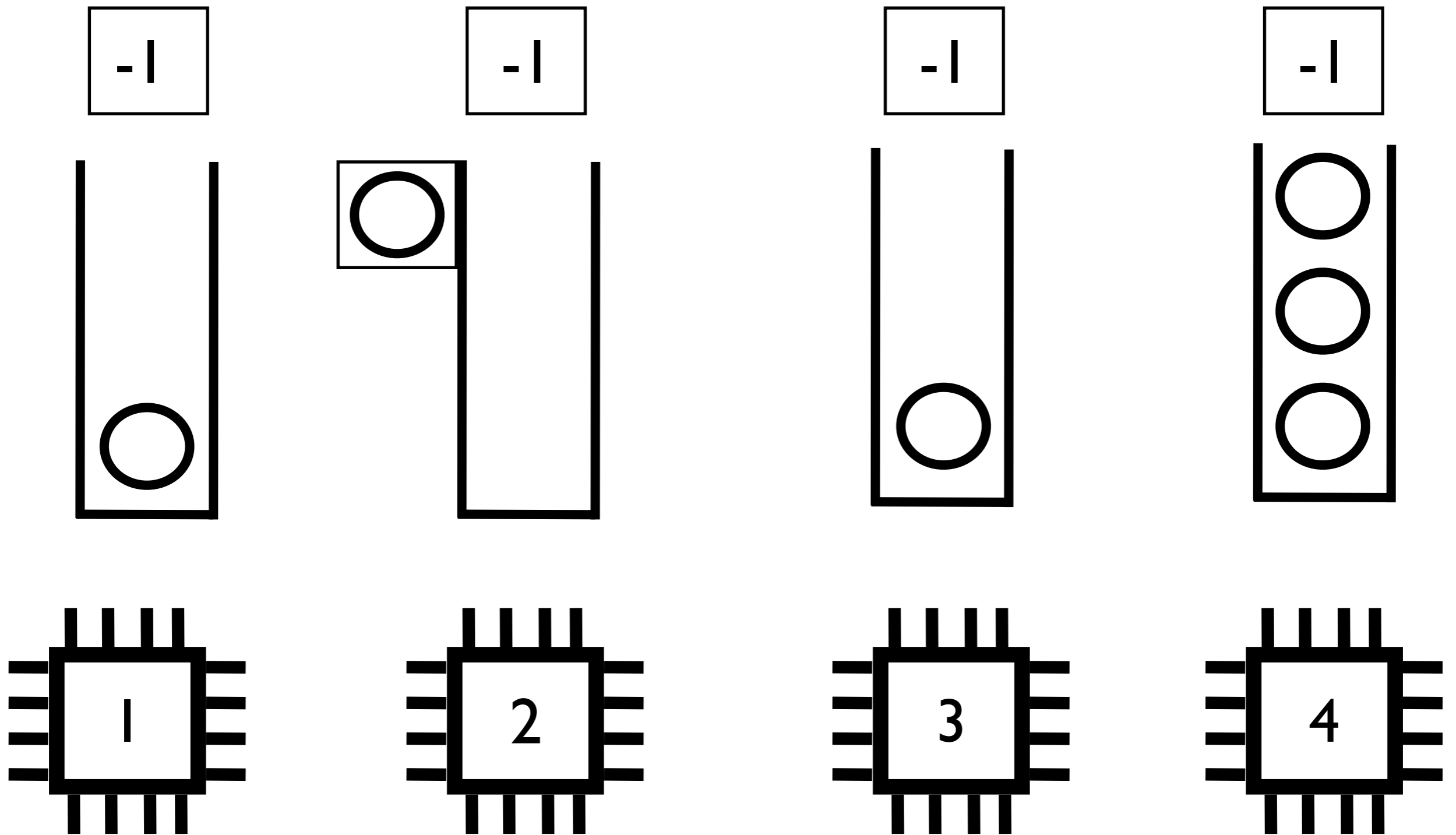
Receiver initiated



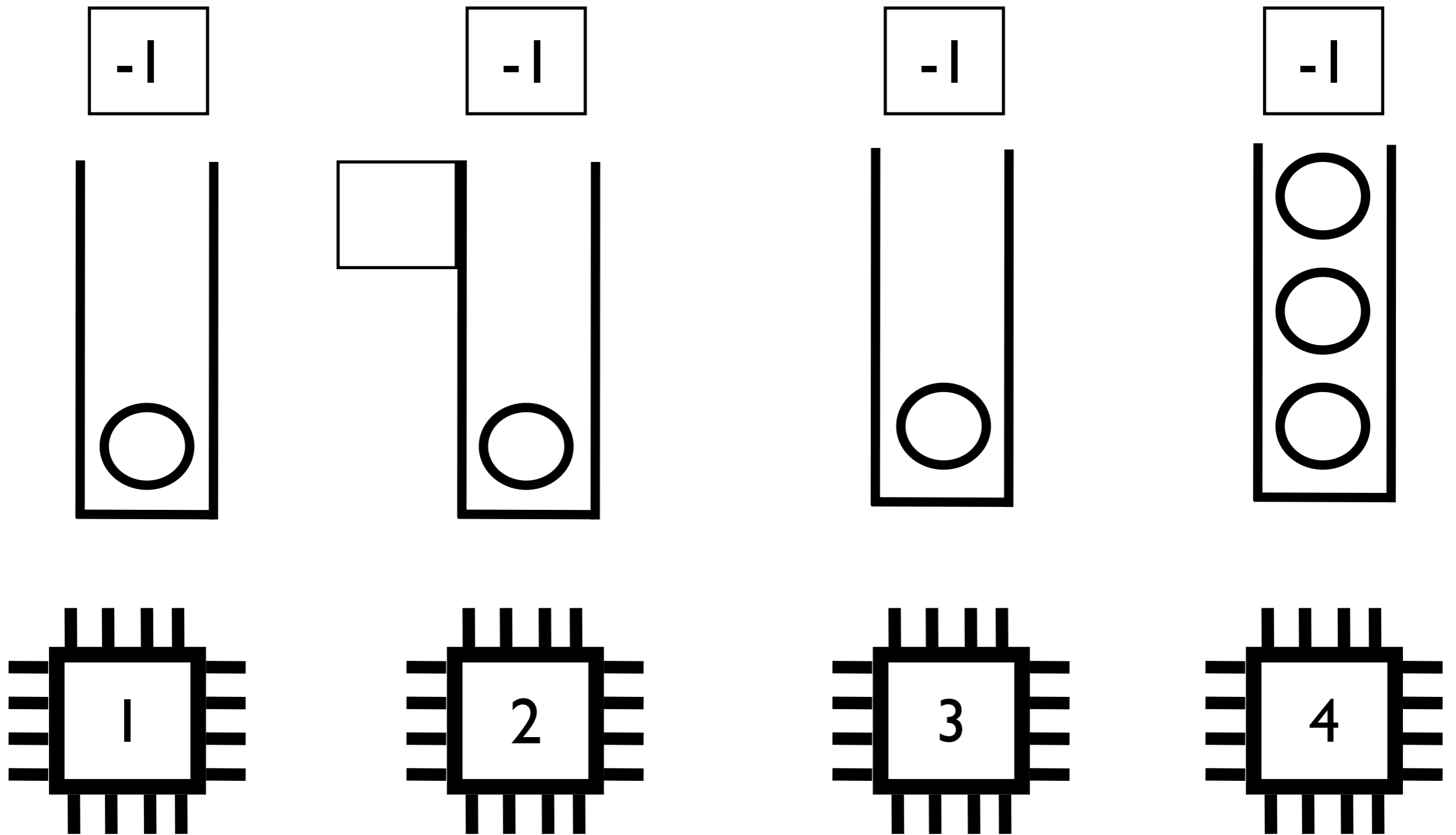
Receiver initiated



Receiver initiated



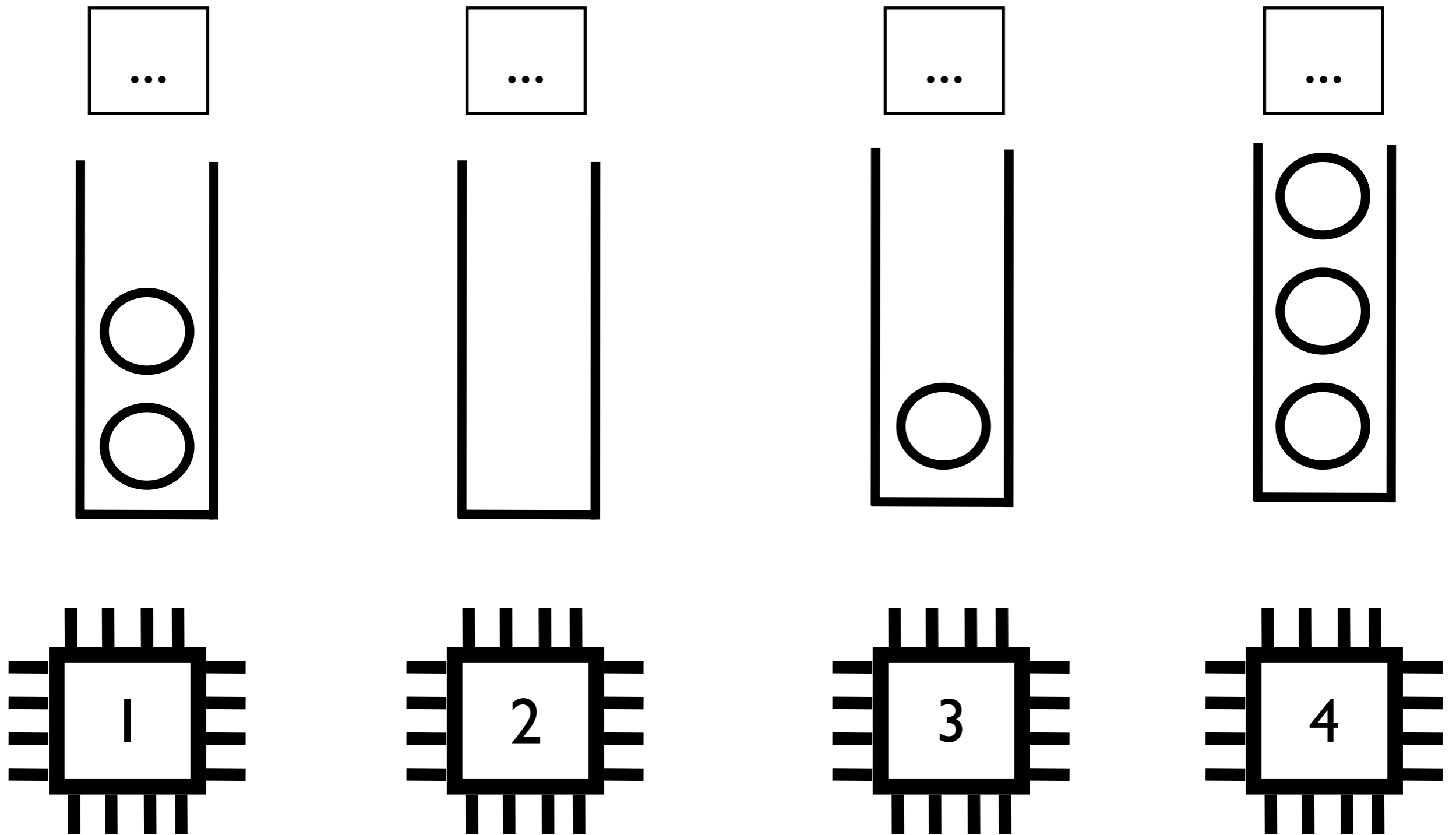
Receiver initiated



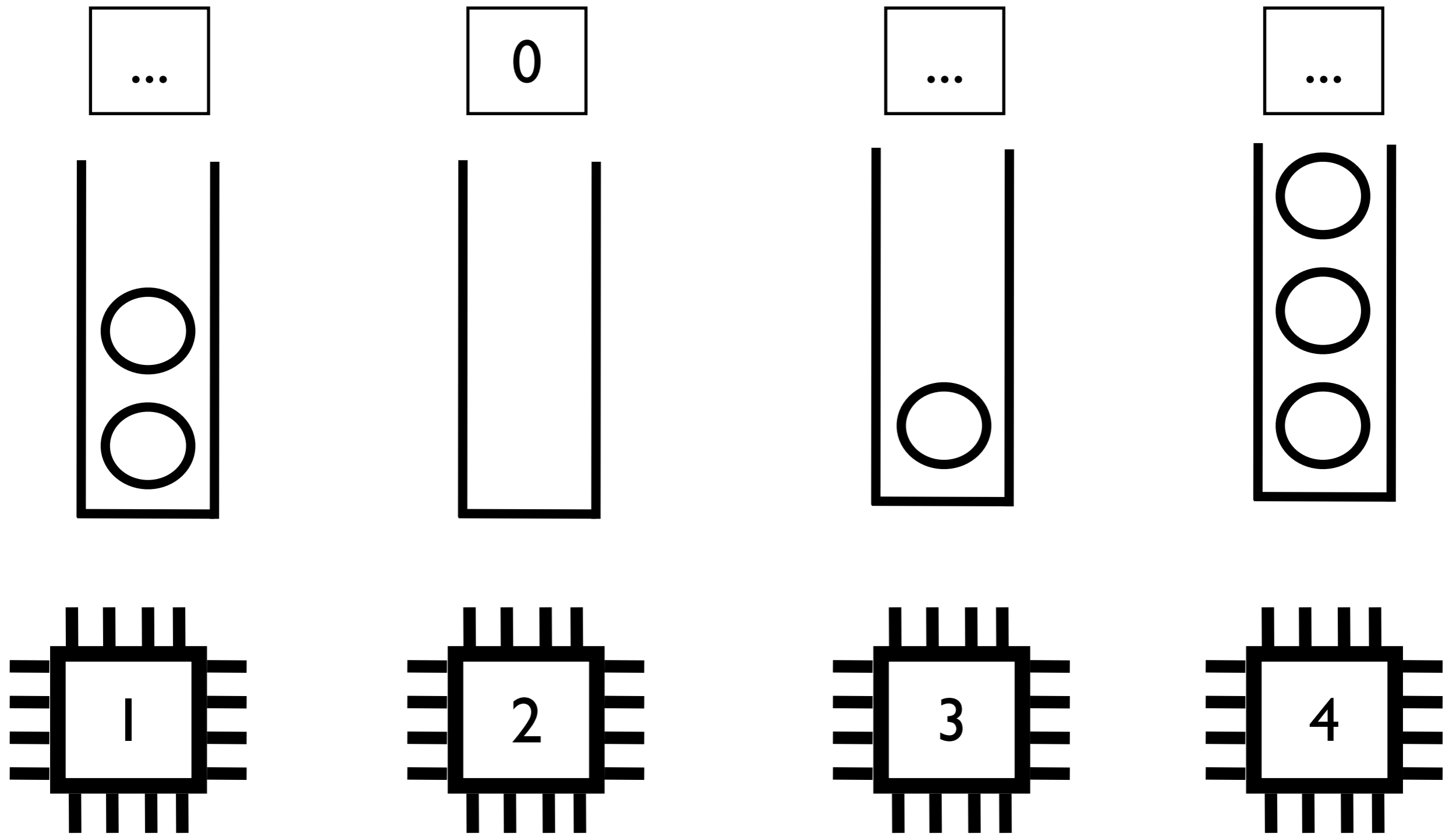
From receiver to sender initiated

- Receiver initiated: each idle core targets one busy core at random
- Sender initiated: each busy core targets one core at random
- Sender initiated idea is adapted from distributed computing.
- Sender initiated is simpler to implement.

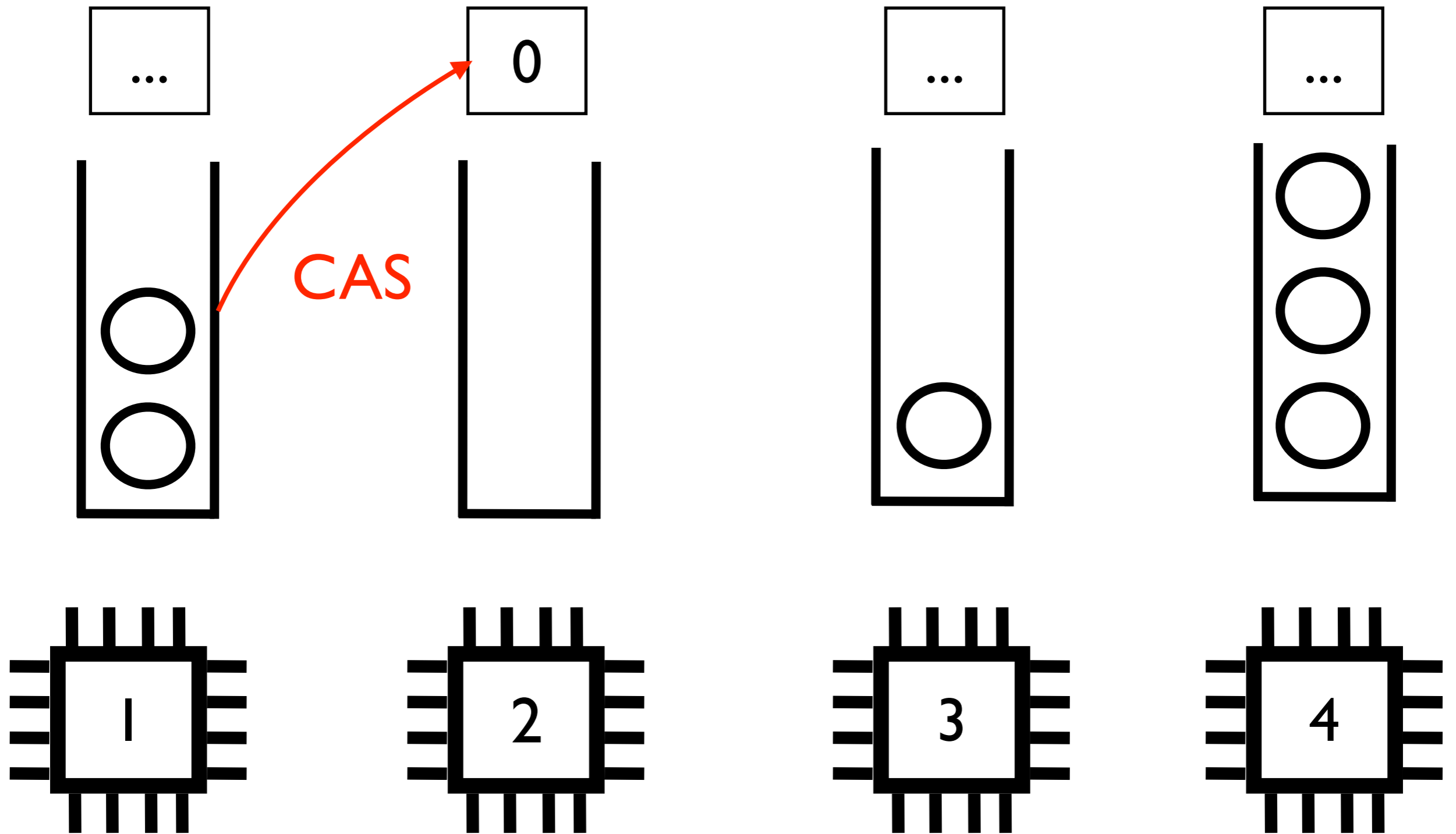
Sender initiated



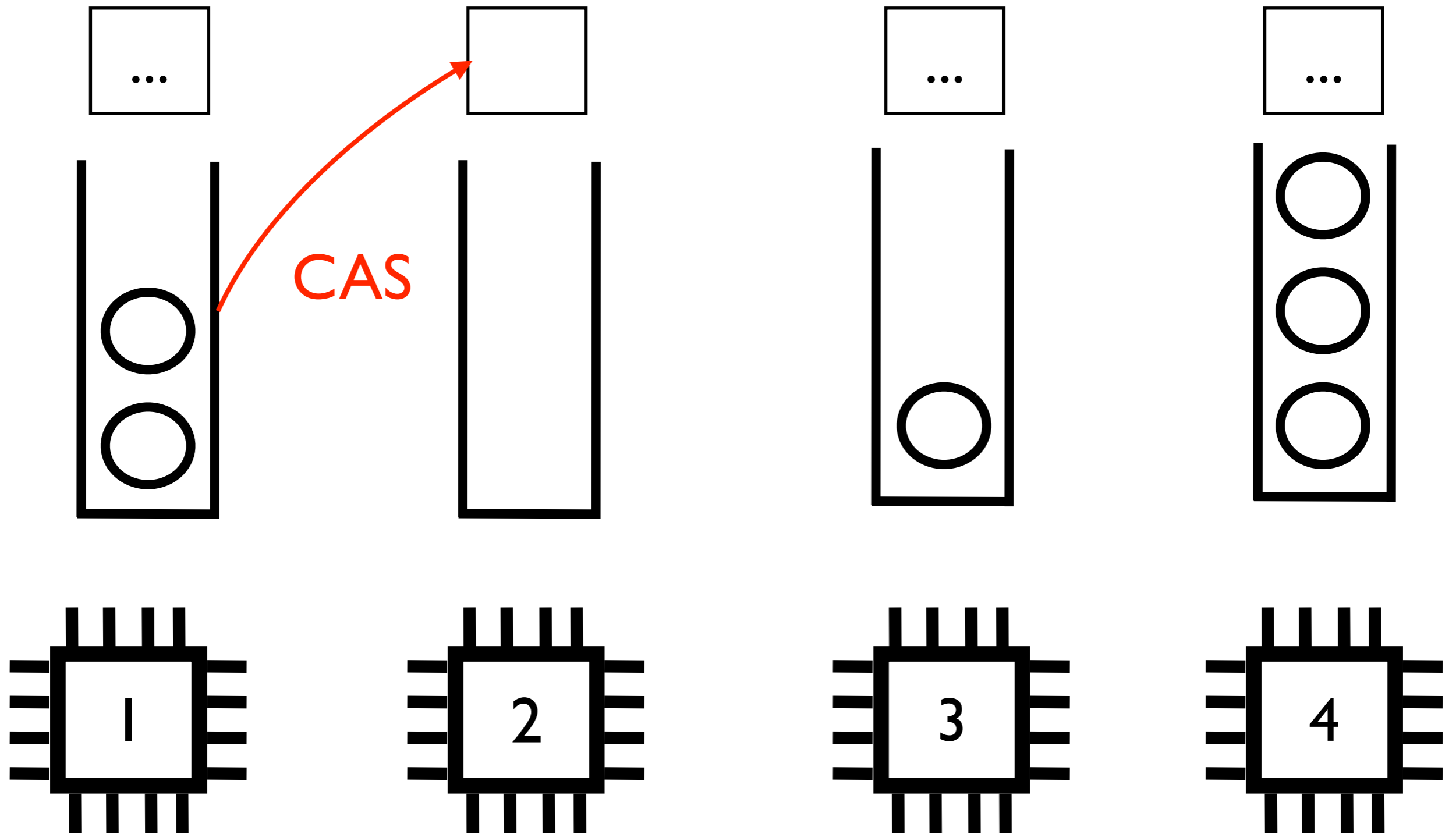
Sender initiated



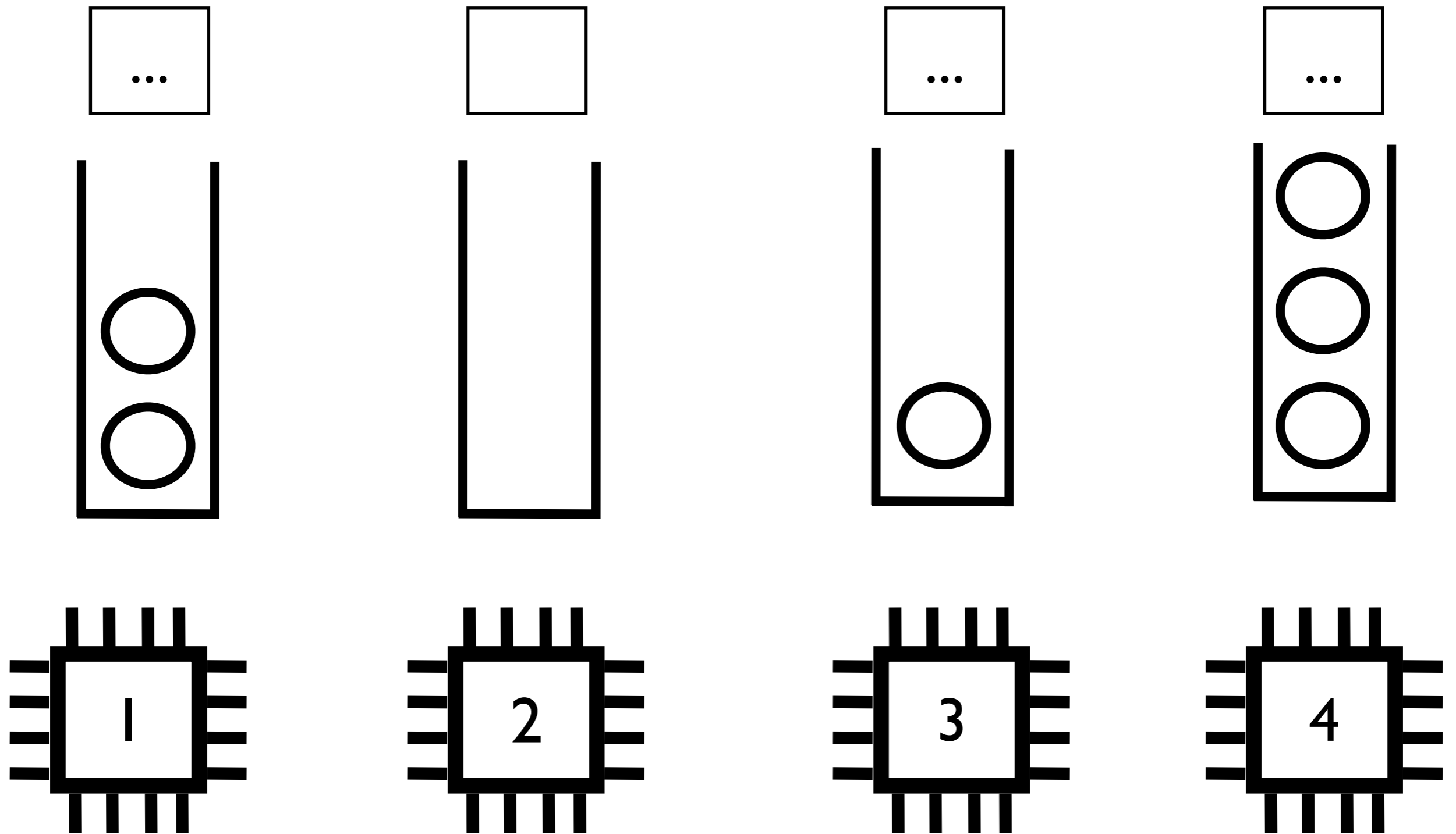
Sender initiated



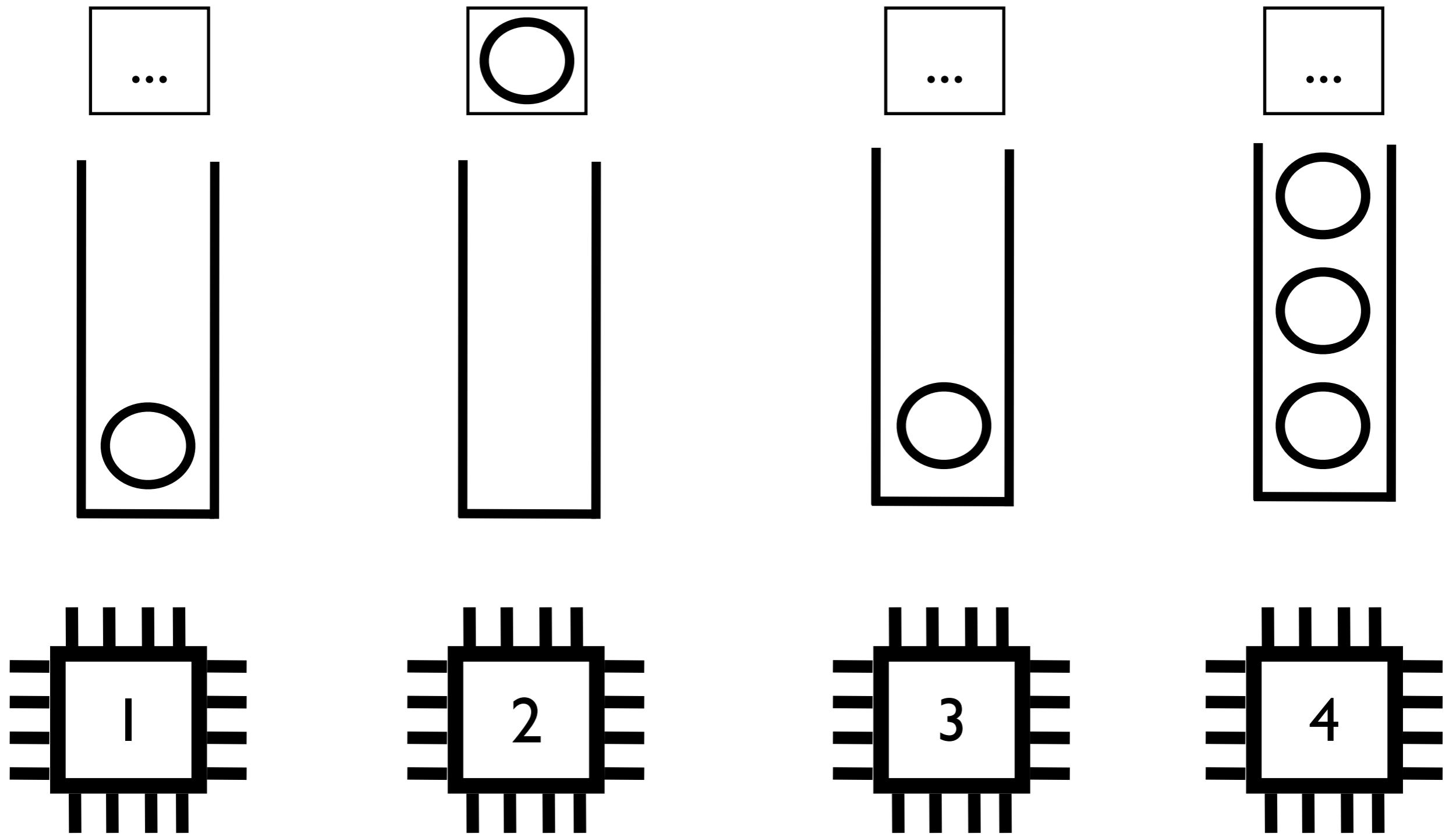
Sender initiated



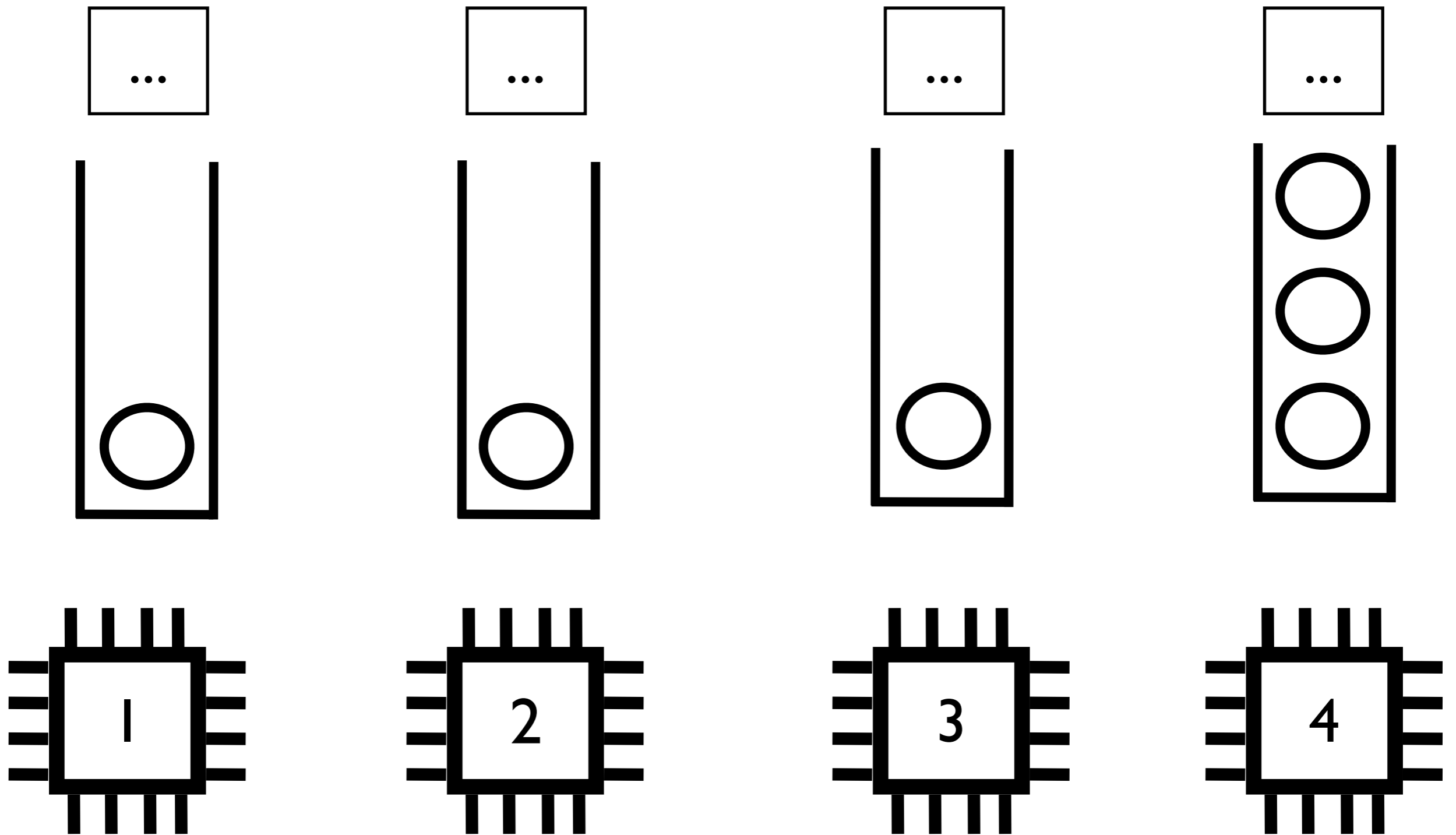
Sender initiated



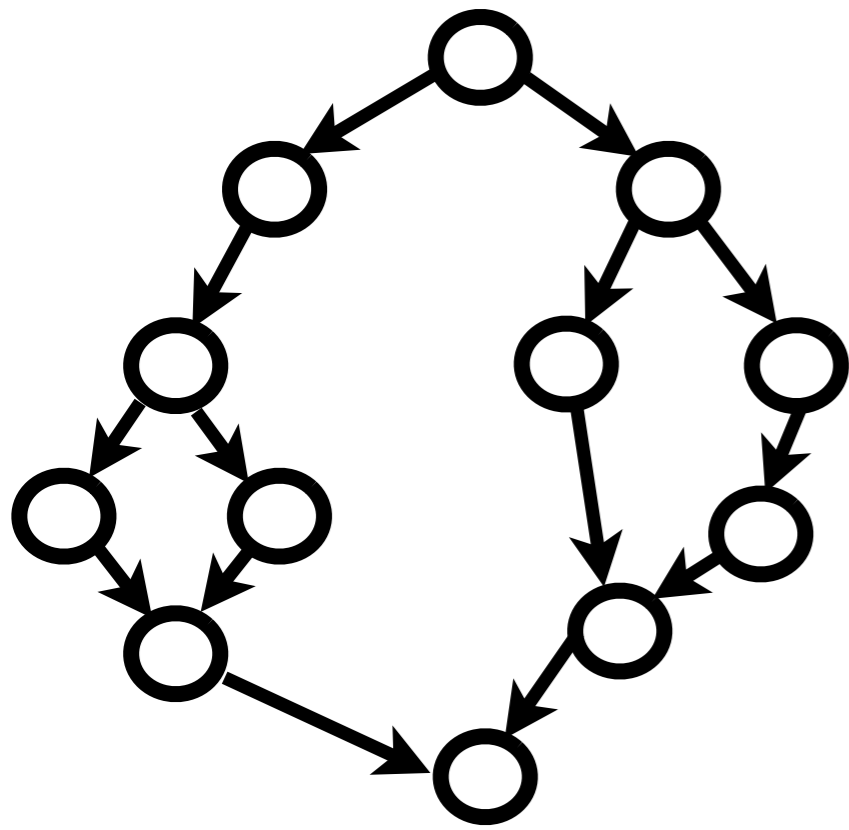
Sender initiated



Sender initiated



Analytical model



P number of cores

T_1 serial run time

T_∞ minimal run time with infinite cores

T_P parallel run time with P cores

δ polling interval

F maximal number of forks in a path

Our main analytical result

Bound for greedy schedulers:

$$T_P \leq \frac{T_1}{P} + \frac{P-1}{P} T_\infty$$

Bound for concurrent dequeues (ignoring cost of fences):

$$\mathbb{E}[T_P] \leq \frac{T_1}{P} + \frac{P-1}{P} T_\infty + O(F)$$

Bound for our two algorithms:

$$\mathbb{E}[T_P] \leq \left(\frac{T_1}{P} + \frac{P-1}{P} T_\infty + O(\delta F) \right) \cdot \left(1 + \frac{O(1)}{\delta} \right)$$

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Bound for concurrent dequeues (ignoring cost of fences):

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Bound for our two algorithms:

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Performance study

- We implemented in PASL:
 - our receiver-initiated algorithm
 - our sender-initiated algorithm
 - our Chase-Lev implementation
- We compare all of those implementations against Cilk Plus.

Benchmarks

- Classic Cilk benchmarks and Problem Based Benchmark Suite (Blelloch et al 2012)
- Problem areas: merge sort, sample sort, maximal independent set, maximal matching, convex hull, fibonacci, and dense matrix multiply.

Performance results

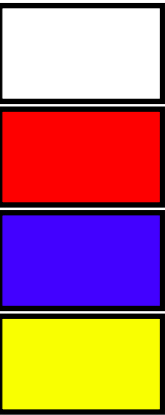
Intel Xeon, 30 cores
polling period = 30 μ sec

concurrent dequeues

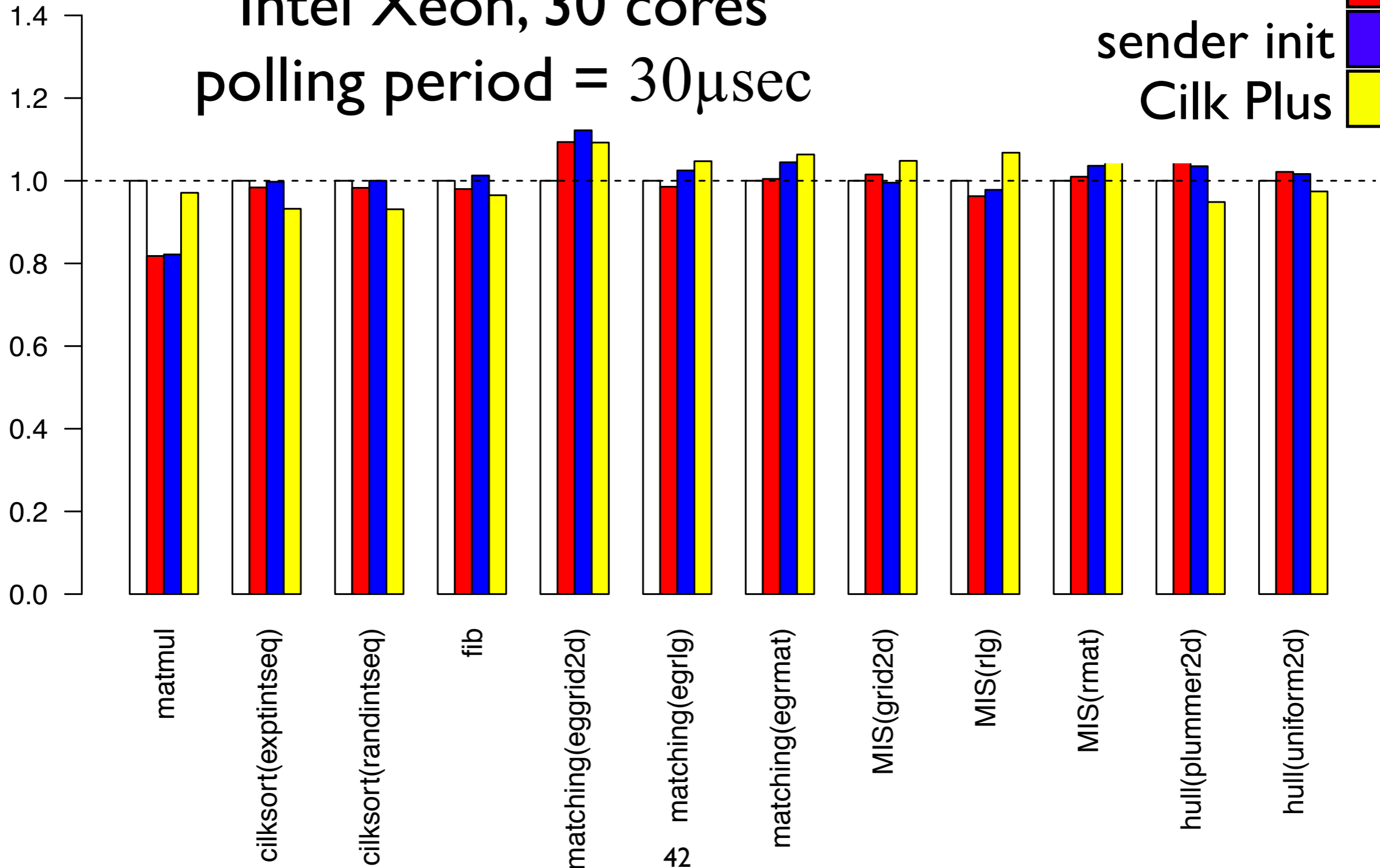
receiver init

sender init

Cilk Plus



Normalized run time



Summary

- We presented two new private-deque algorithms, evaluated them, and proved analytical results.
- In the paper, we demonstrated the flexibility of private deques by implementing the steal half policy.

Our papers

- Efficient primitives for creating and scheduling parallel computations
By U.Acar, A. Charguéraud, and Mike Rainey
DAMP'12
- Oracle scheduling: controlling granularity in implicitly parallel languages
By U.Acar, A. Charguéraud, and Mike Rainey
OOPSLA'11
- Scheduling parallel programs by work stealing with private deques
By U.Acar, A. Charguéraud, and Mike Rainey
PPoPP'13